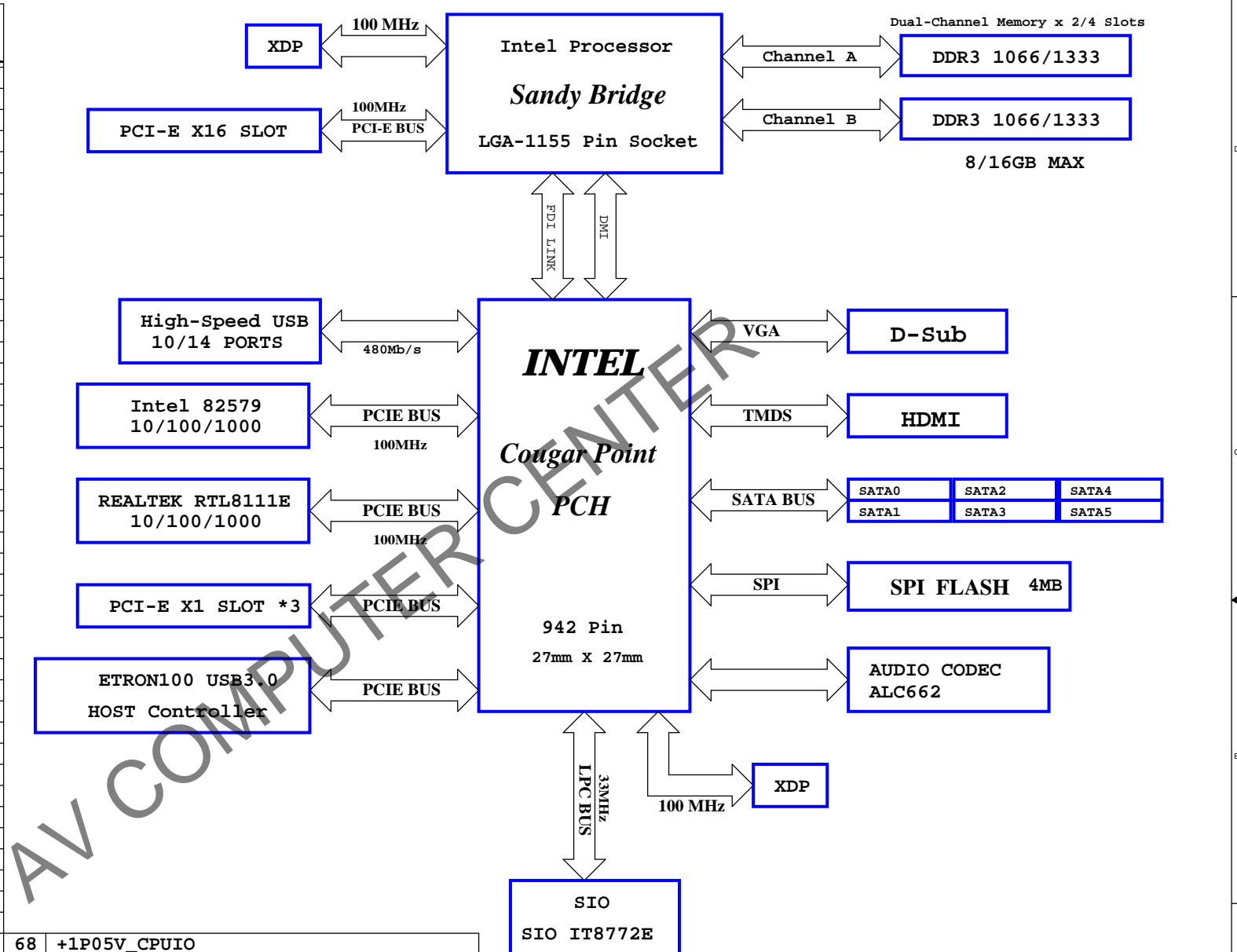


# IPISB-VR

Revision: 1.01

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02	CLOCKS DISTRIBUTION
03	SIGNAL & RESET MAP
04	CHANGE HISTORY
05	POWER FLOW
06	POWER DISTRIBUTION
07	POWER SEQUENCE
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14	PLTRST_CPU#
15~16	DDR3 CHANNEL A&B
17	DDR3 TERMINATION A&B
18	XXXX
19~27	INTEL_PCH(1~9)
28	PCH DPWROK & SUS ACK#
29	INTEGRATED VGA PORT
30	XXXX
31	HDMI CONN
32	PCI EXPRESS X16 SLOT
33	PCI EXPRESS X1 SLOT
34	PCI EXPRESS X1 SLOT
35	RSMRST
36	SUPER I/O IT8772E
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39	REALTEK RTL8111E CONTROLLER
40	RJ45+USB CONNECTOR
41	REAR USB-4 PORTS
42	SIS USB 3.0 CONTROLLER
43	USB 3.0 CONNECTOR
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45	USB POWER
46	USB HEADER - 1
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48	REALTEK ALC662 CODEC
49	FRONT AUDIO CONNECTOR
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51	LPC DEBUG
52	SATA CONNECTOR
53	SM BUS & SPI ROM
54	FRONT PANEL CIRCUIT
55	RTC / CMOS / SPKR/ SCREW
56	CPU XDP DEBUG CONNECTOR
57	PCH XDP DEBUG CONNECTOR
58	FAN CIRCUIT
59	KB & MS
60	ATX POWER_24P CONNECTOR
61	+3VA & +3VSB & +5VSB
62	+1P5V DUAL & +1P8V SFR
63	+VTT_DDR & +1P5V_DUAL_EN
64	+1P05V_PCH & +0P925V_SA
65	+5V_DUAL & +5V_DUAL_USB_B/F
66	XXXX
67	+1P8V&+3P3V_LAN & +3P3V_ME



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68	+1P05V_CPUIO
69	+1P05V_ME&+1P05V_CPUIO CAP
70	VCORE CONTROLLER
71	VCORE DRIVER 1-2
72	VCORE DRIVER 2-2
73	VCORE CAP
74	+V_AXG DRIVER
75	OV CONTROLLER
76	EMI CAP

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## Schematics Change History

[illegible]

**CAD Note:**

Property: BOM

I = Installed Part.

NI = Not Installed Part.

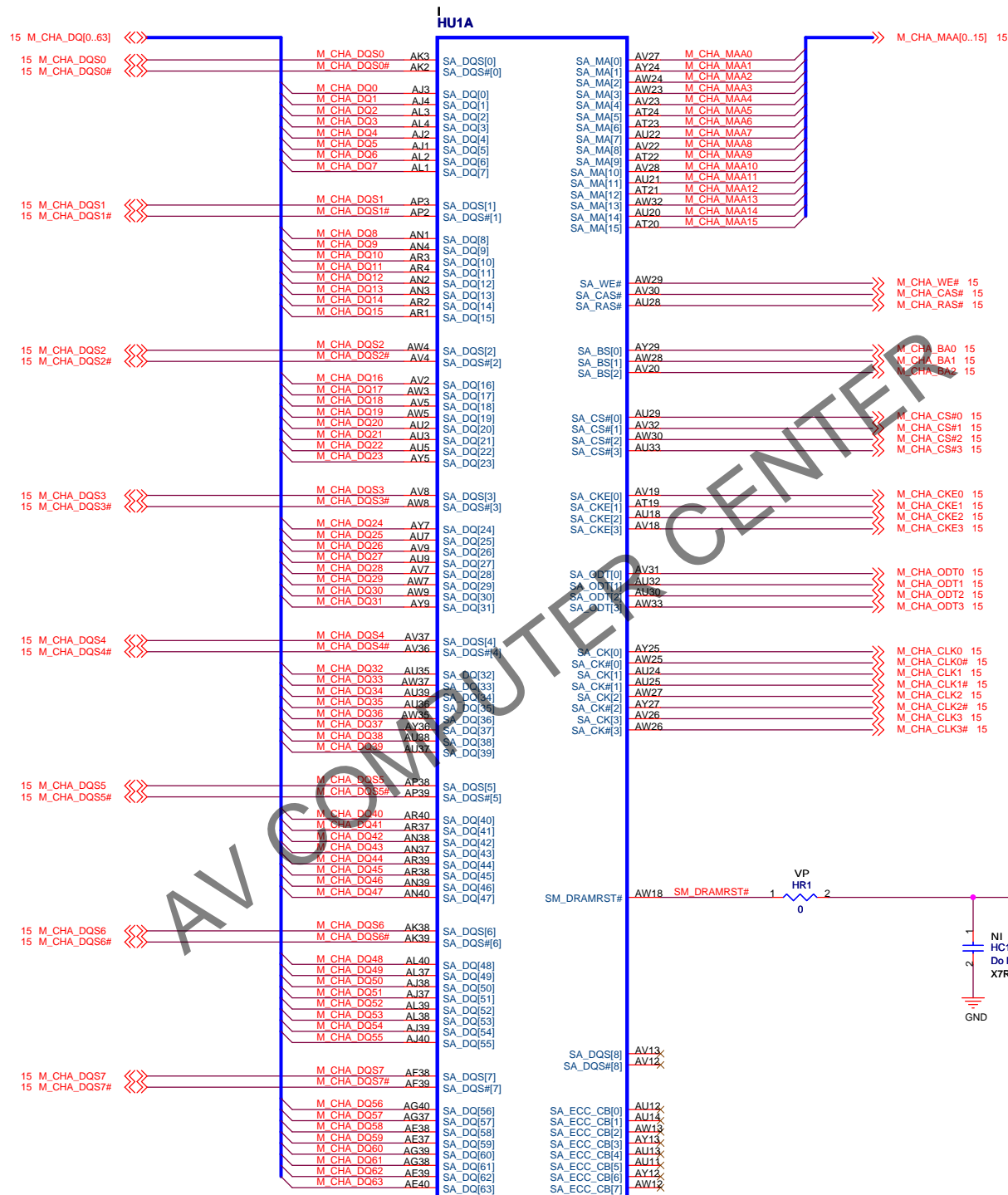
PROTO = PROTO Phase Only.

VP = Virtual Part.

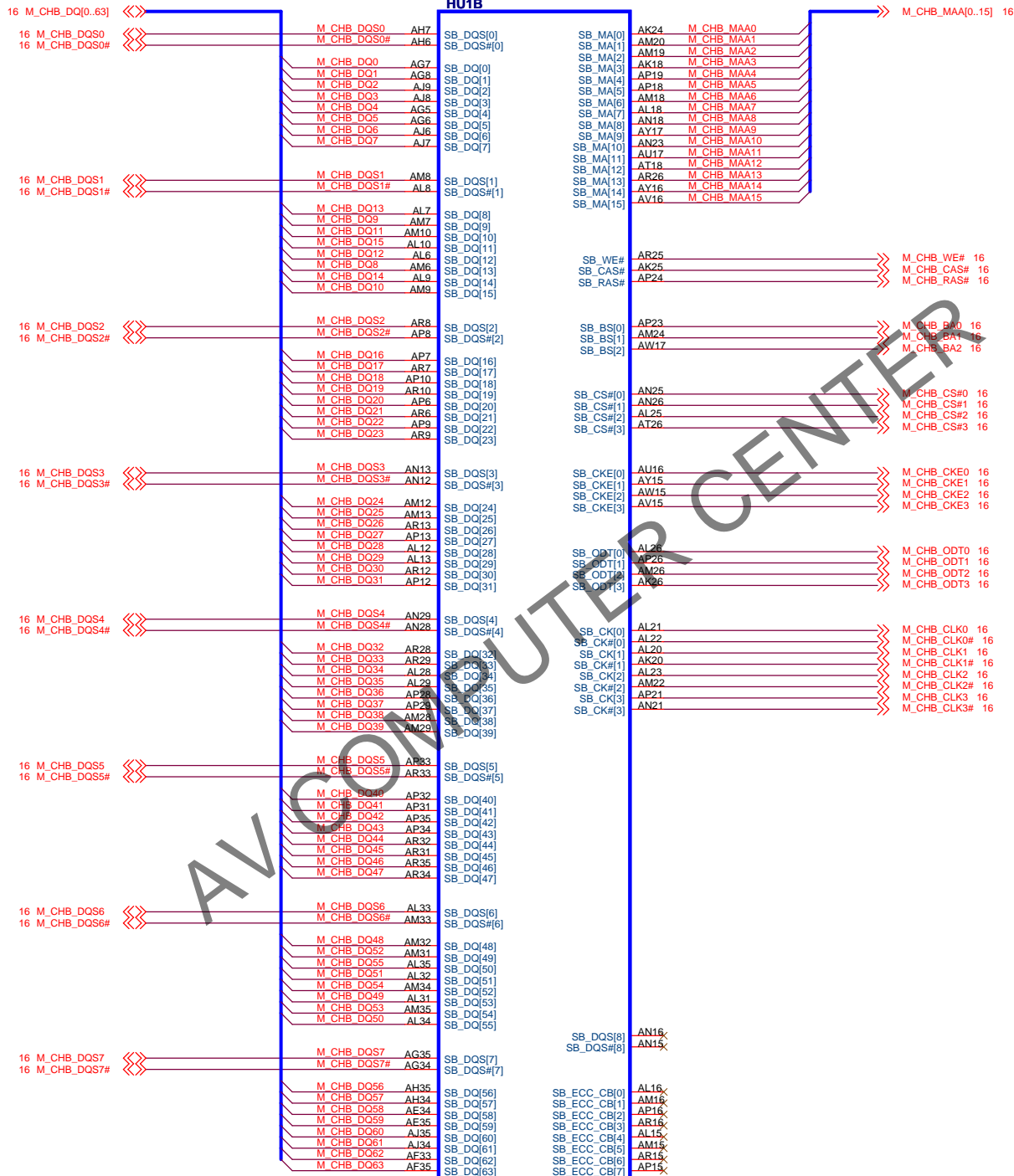
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DDR3\_B

SOCKET\_1155P\_Rev1.1

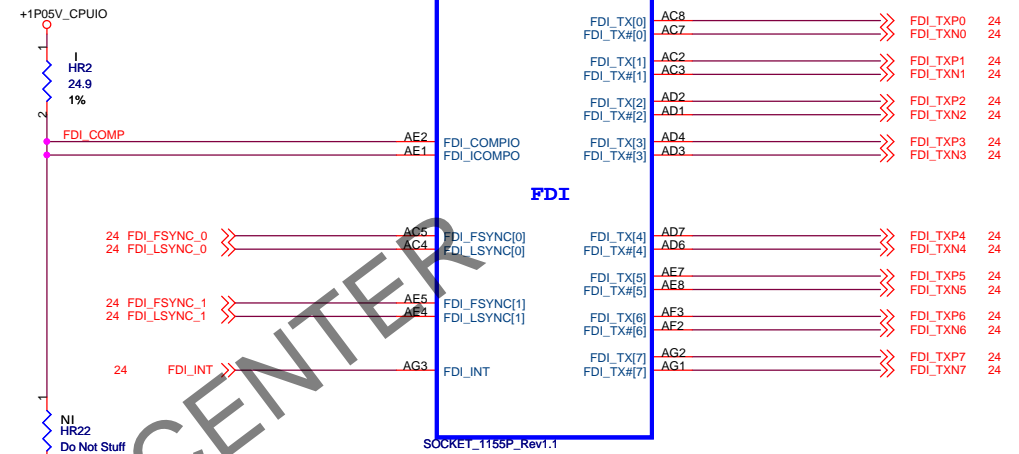
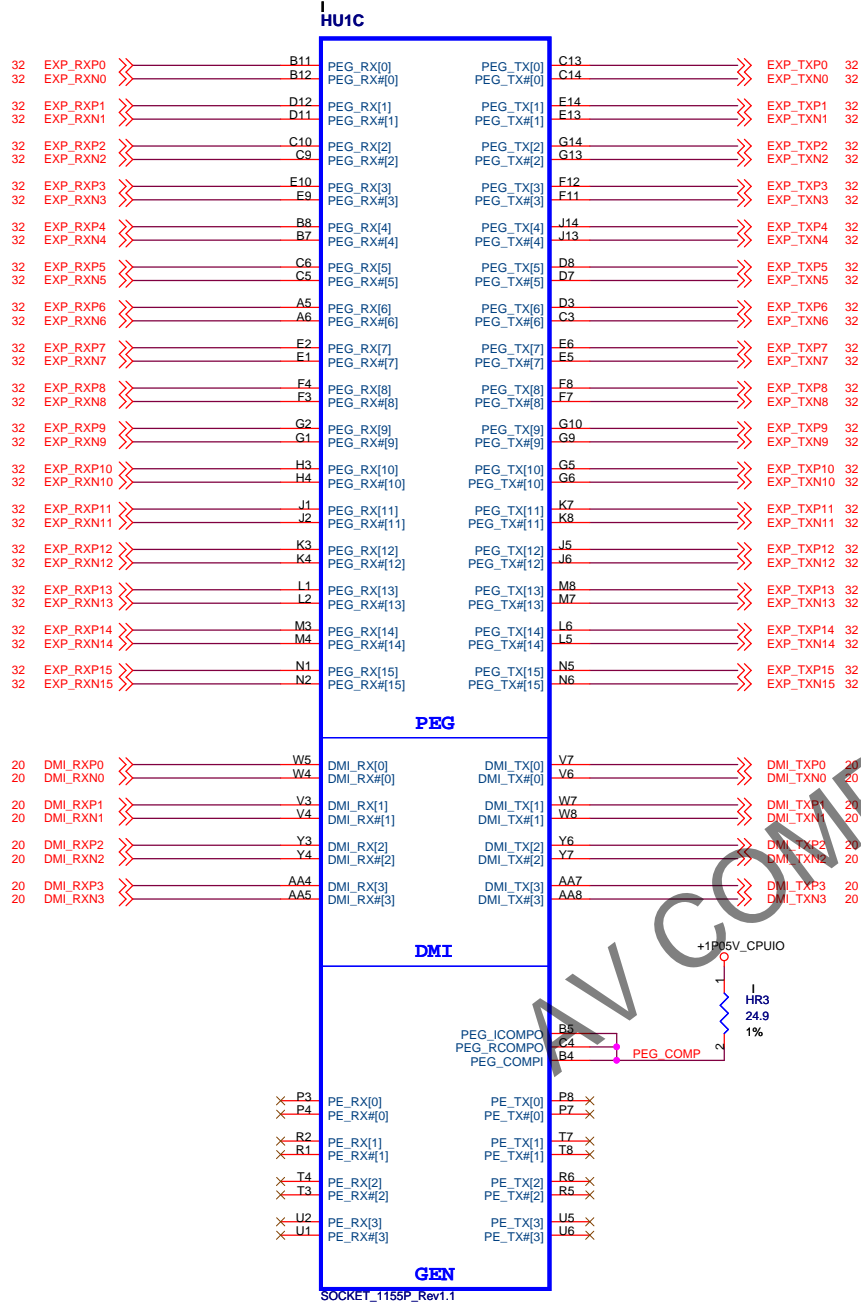
PEGATRON DT-MB RESTRICTED SECRET

**PEGATRON** Title : **DDR3\_B 2-6**

Pegatron Corp. Engineer: **Tim Chen**

Size	Project Name	Rev
A3	<b>IPISB-VR</b>	1.01

Date: Wednesday, December 08, 2010 Sheet 9 of 76



PEGATRON DT-MB RESTRICTED SECRET

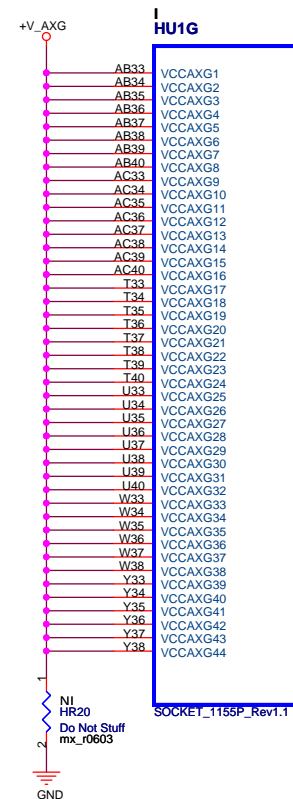
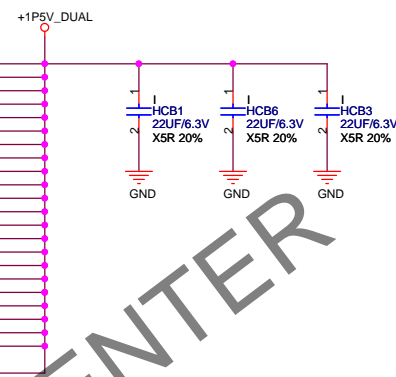
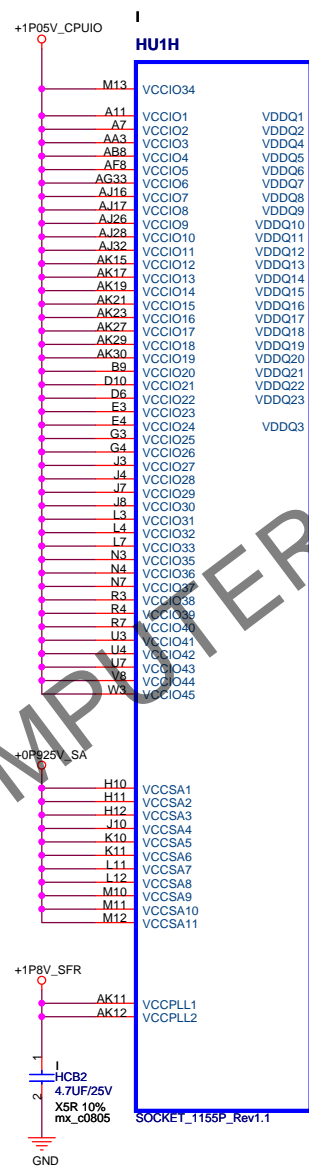
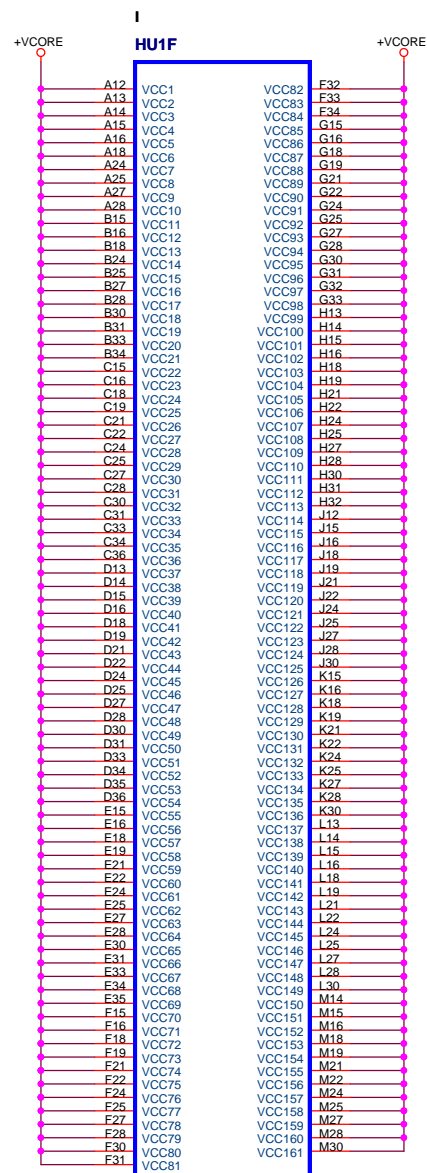
**PEGATRON** Title : **PCIE/DMI/FDI 3-6**

Pegatron Corp. Engineer: **Tim Chen**

Size	Project Name	Rev
A3	<b>IPISB-VR</b>	1.01

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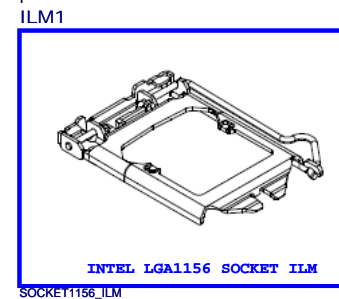
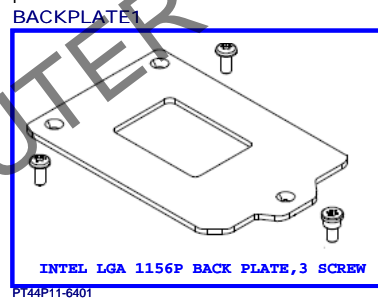
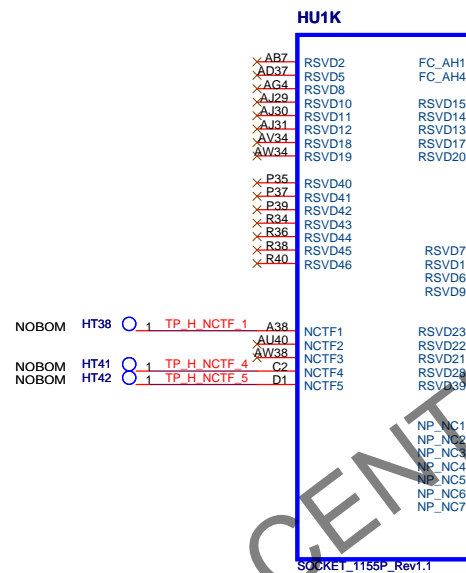
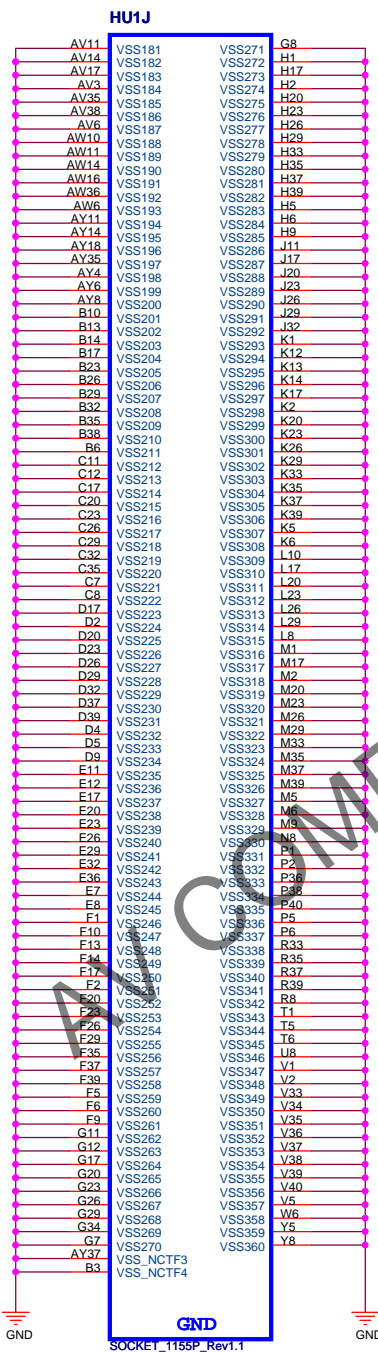
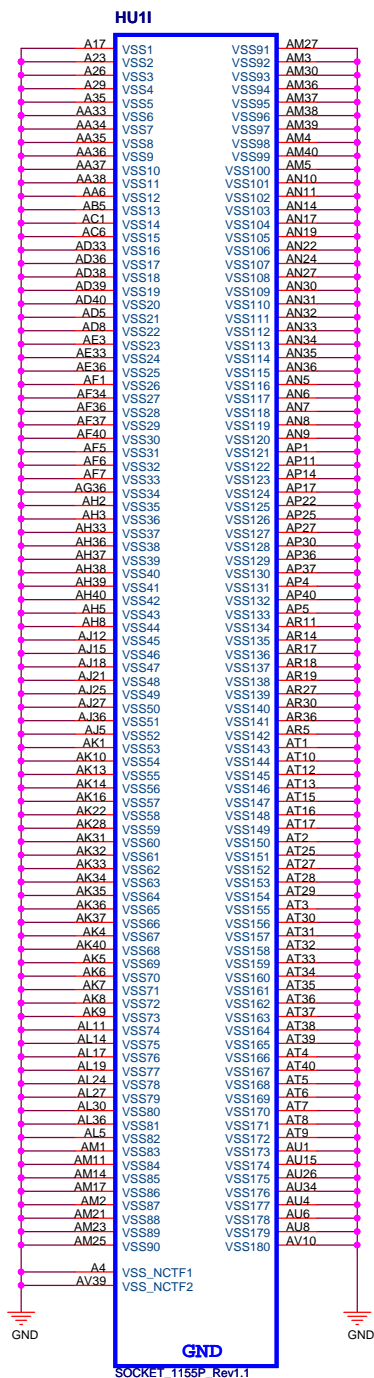
PEGATRON DT-MB RESTRICTED SECRET

**PEGATRON** Title : VCC 5 - 6

Pegatron Corp. Engineer: Tim Chen

Size	Project Name	Rev
A3	IPISB-VR	1.01

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PEGATRON DT-MB RESTRICTED SECRET

**PEGATRON** Title : VSS 6 - 6

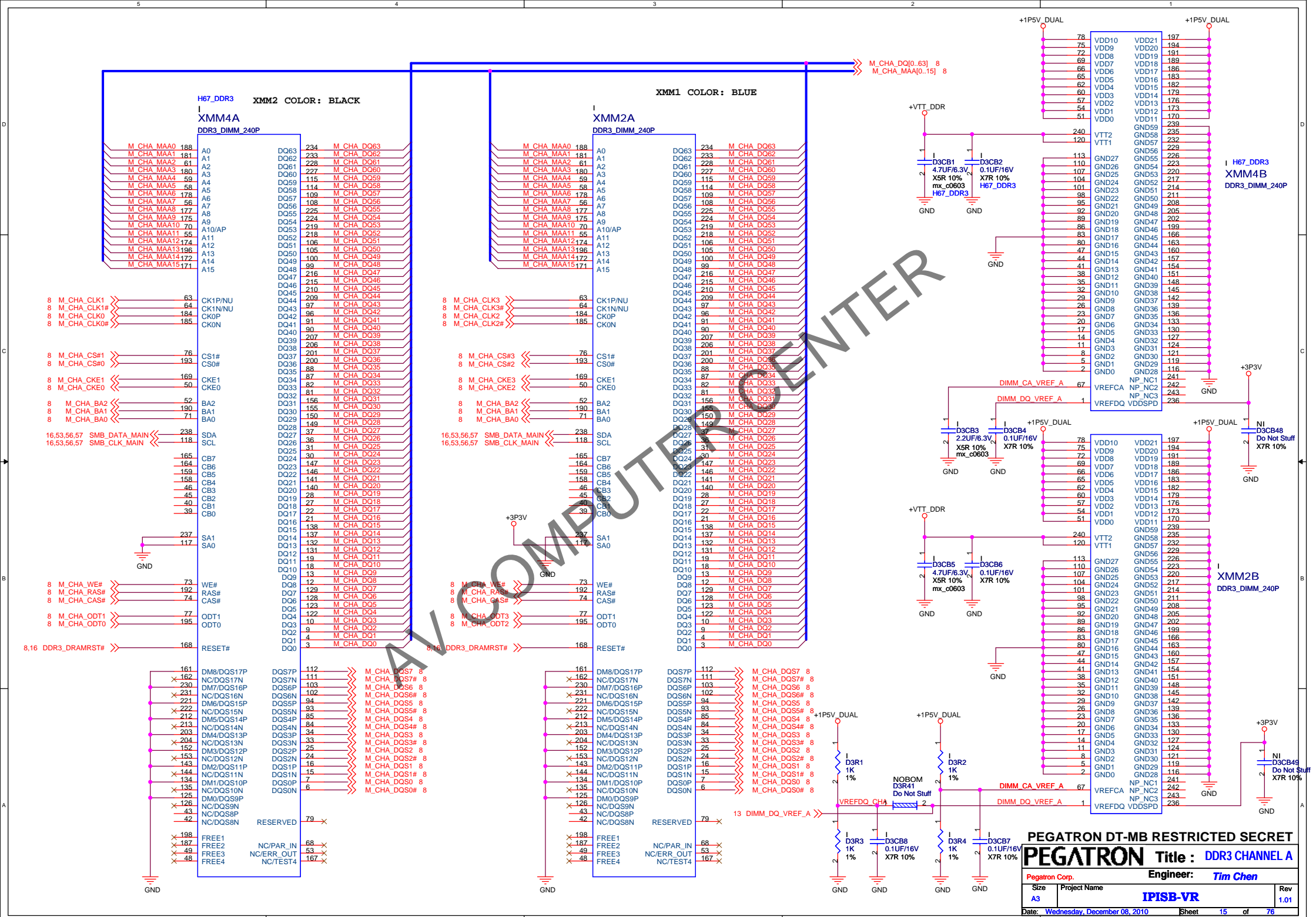
Pegatron Corp. Engineer: Tim Chen

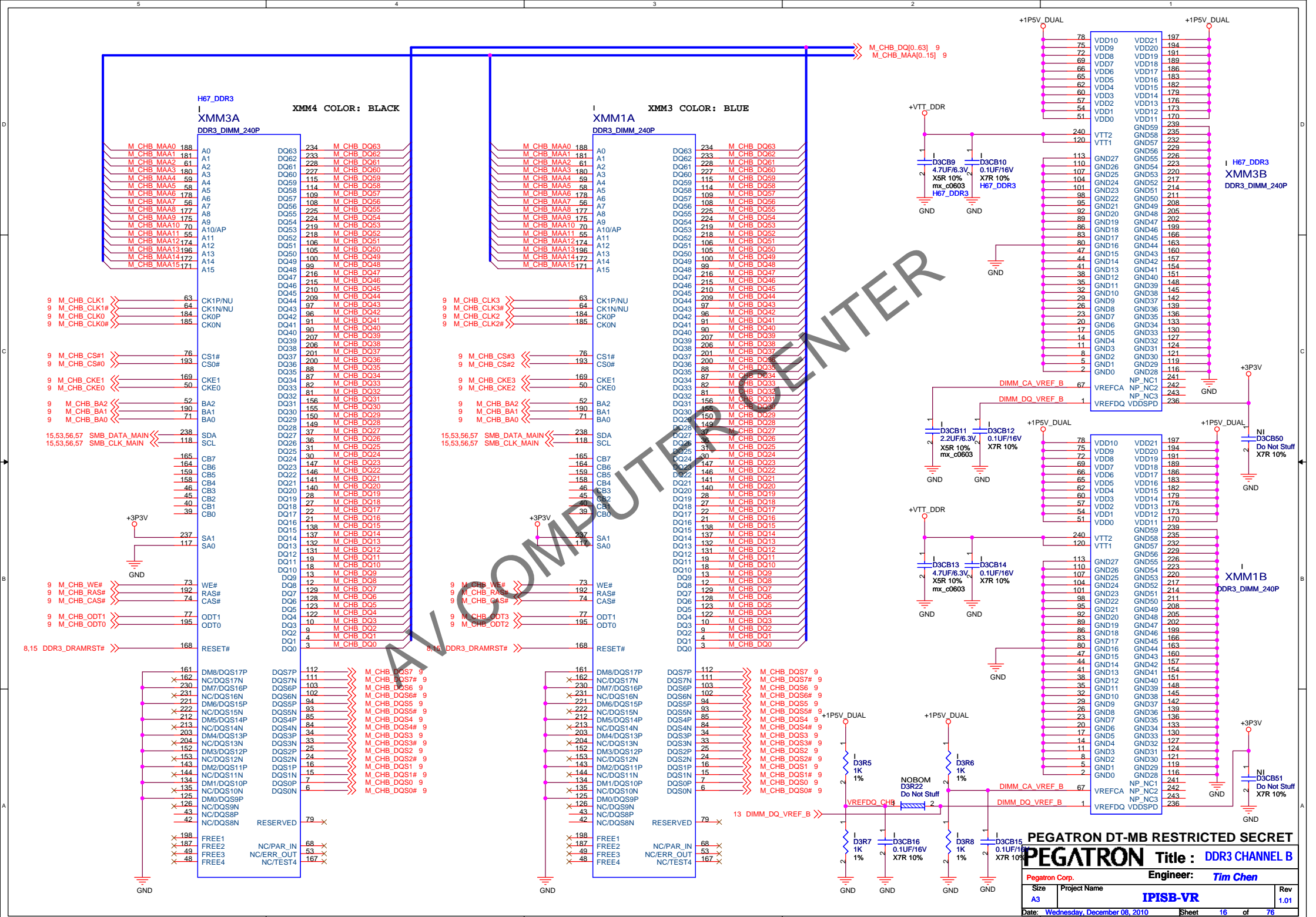
Size A3 Project Name IPISB-VR Rev 1.01

Date: Wednesday, December 08, 2010 Sheet 13 of 76













PEGATRON DT-MB RESTRICTED SECRET

**PEGATRON** Title : DDR3 TERMINATION A&B

Pegatron Corp. Engineer: **Tim Chen**

Size A3	Project Name <b>IPISB-VR</b>	Rev 1.01
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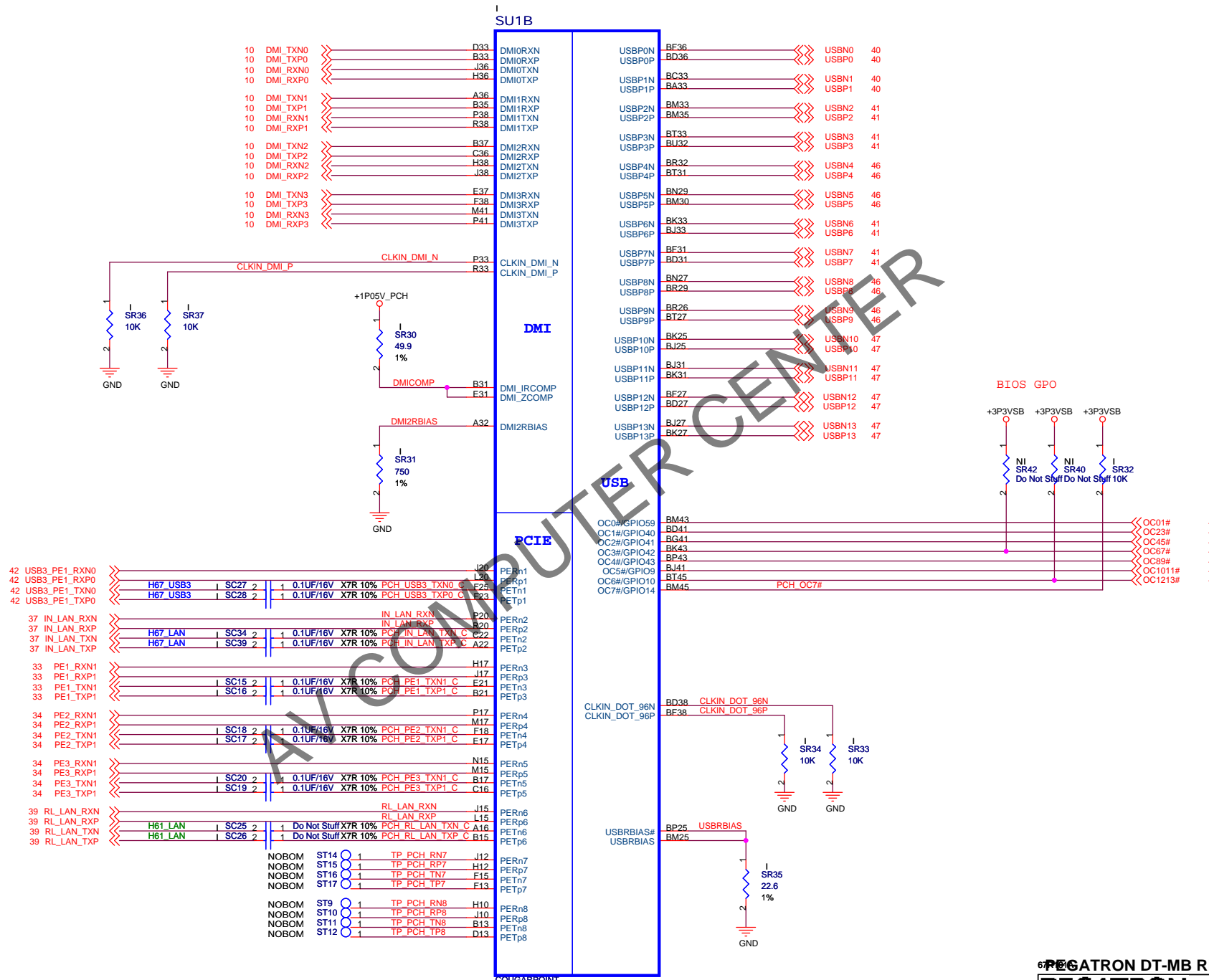
Date: Wednesday, December 08, 2010 Sheet 17 of 76

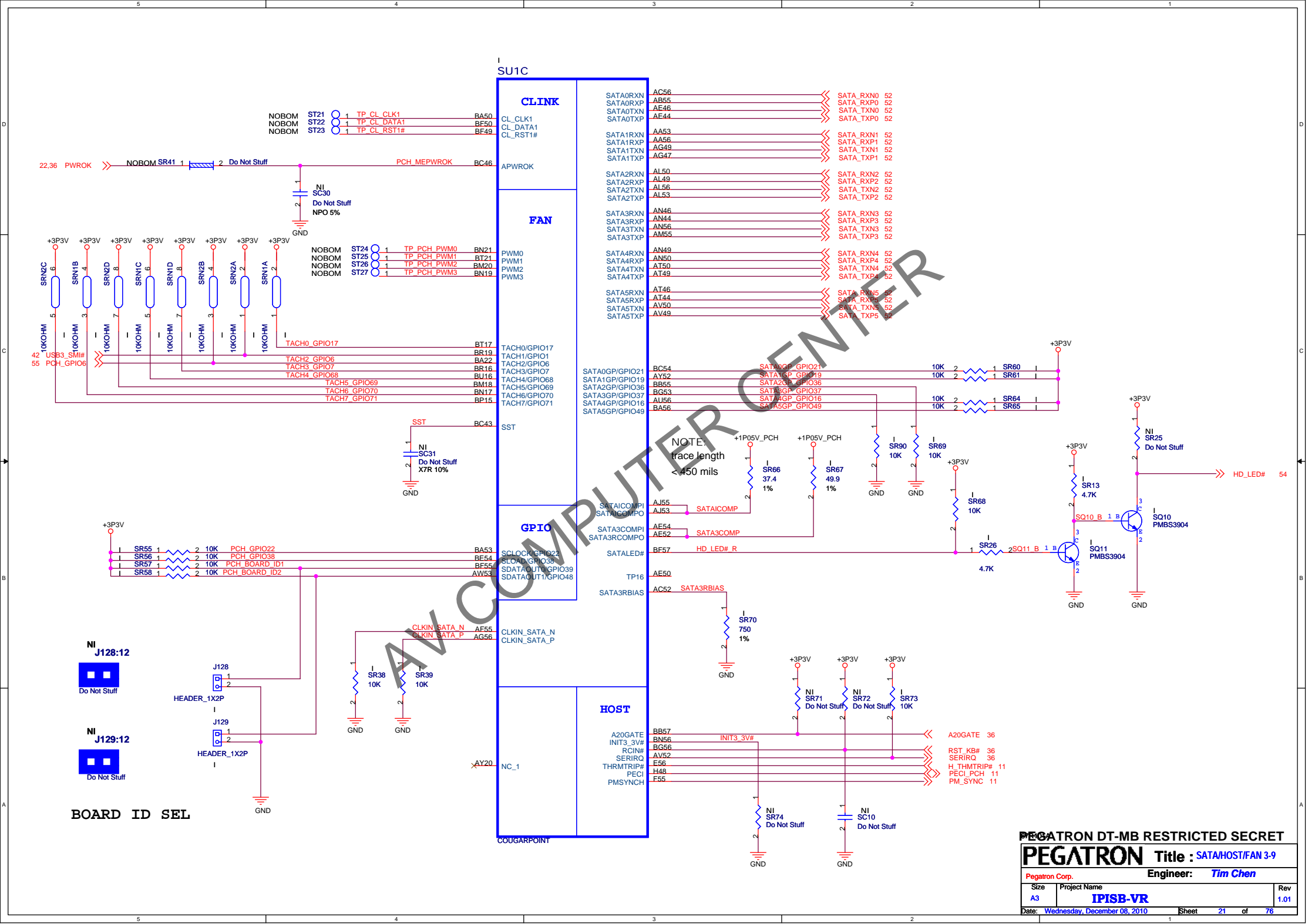
AV COMPUTER CENTER

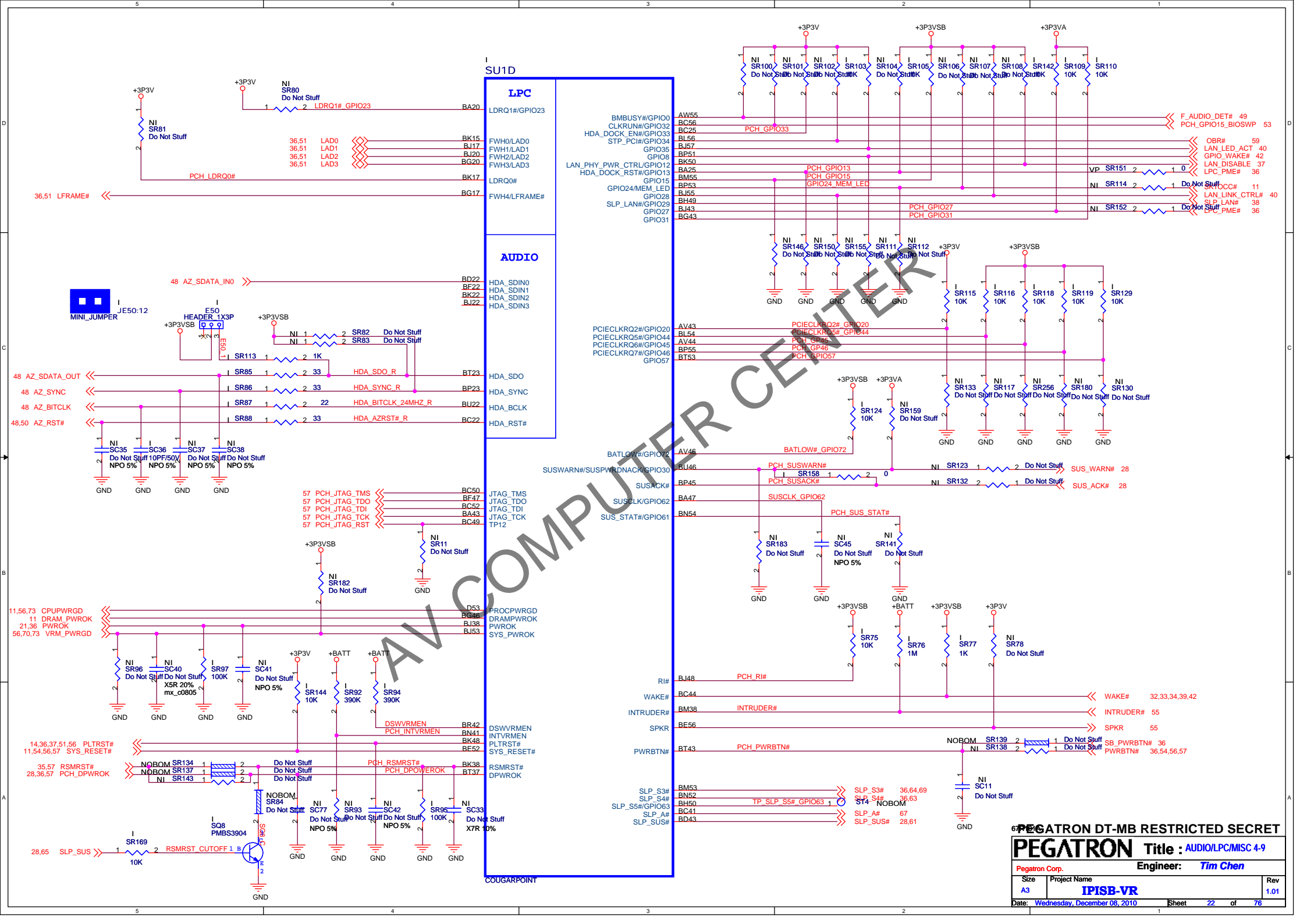
PEGATRON DT-MB RESTRICTED SECRET

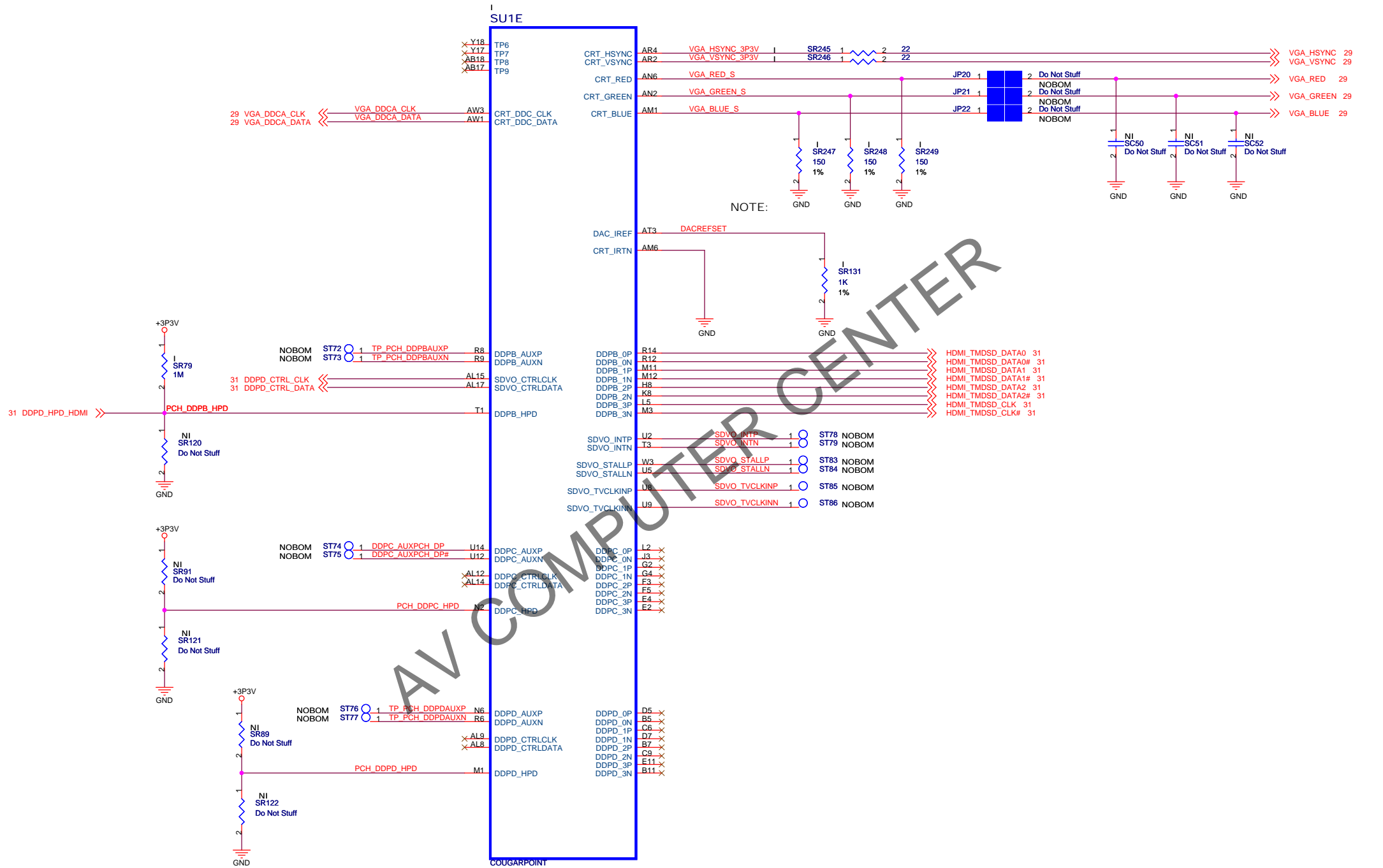
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Pegatron Corp.		Engineer: Tim Chen	
Size A3	Project Name <b>IPISB-VR</b>	Rev 1.01	
Date: Wednesday, December 08, 2010		Sheet 18	of 76

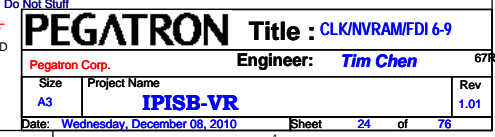




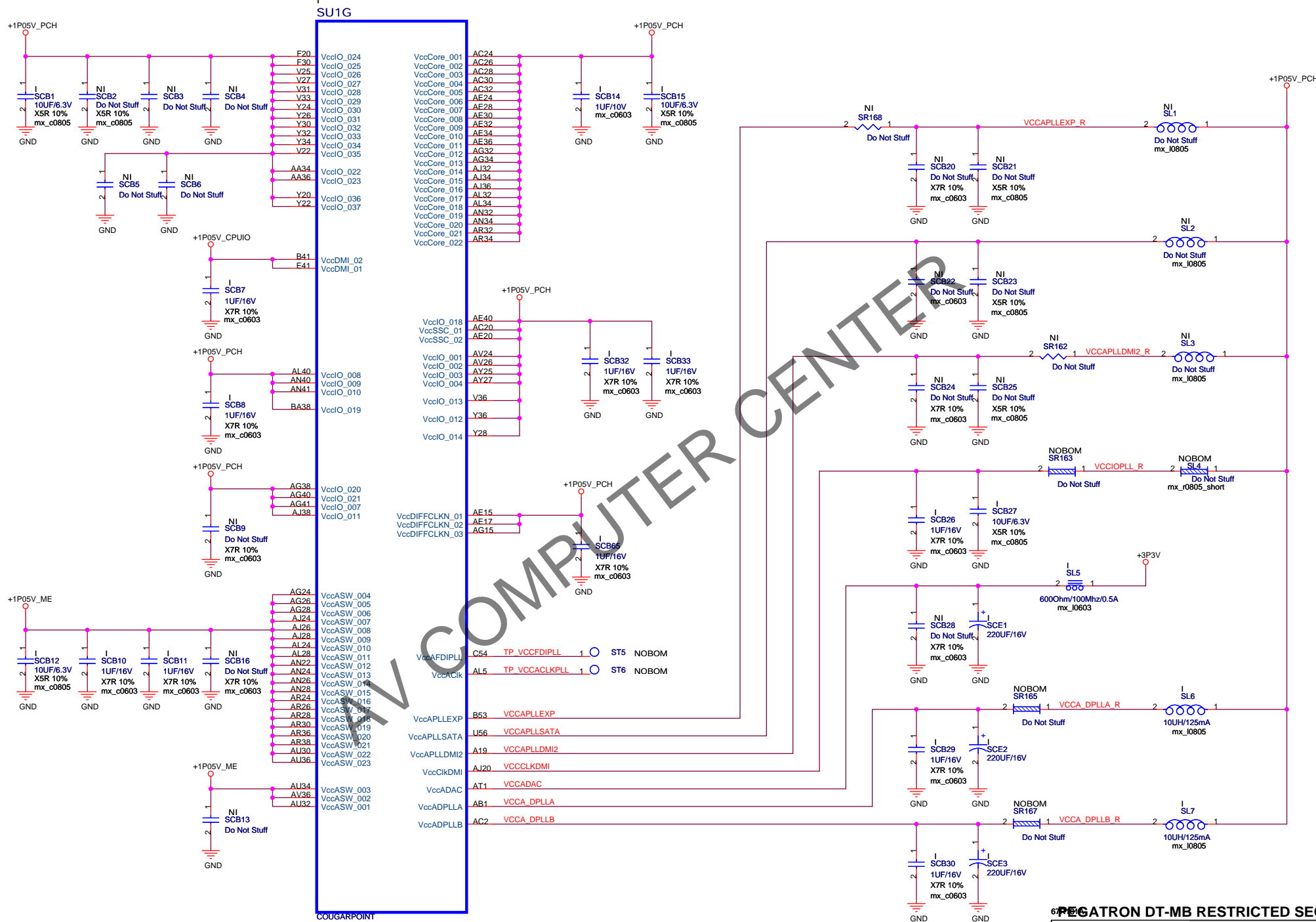




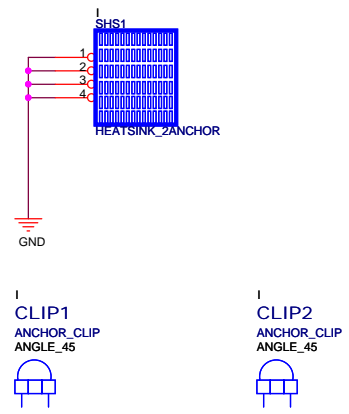
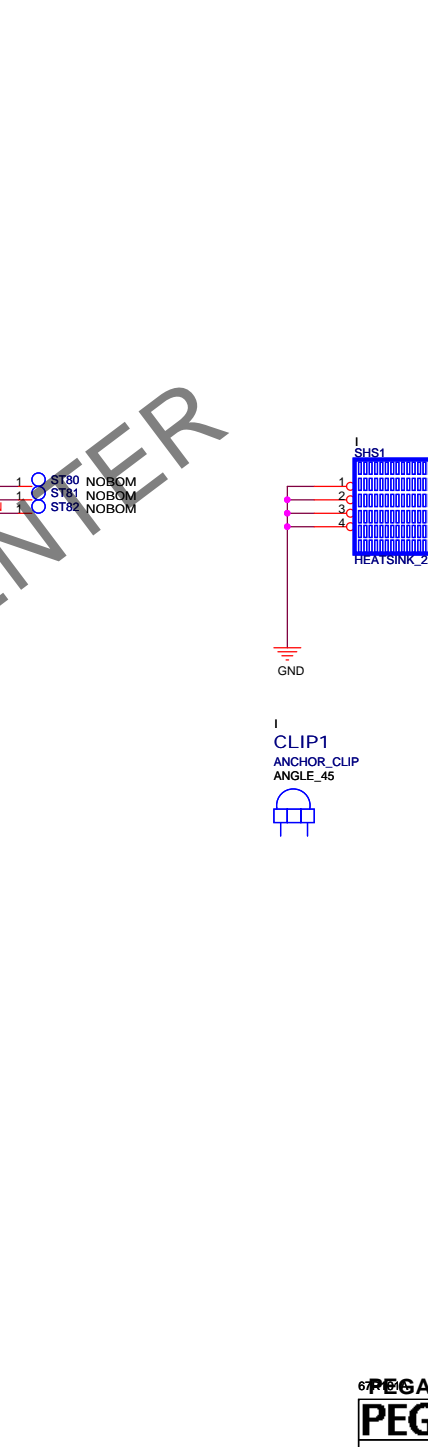
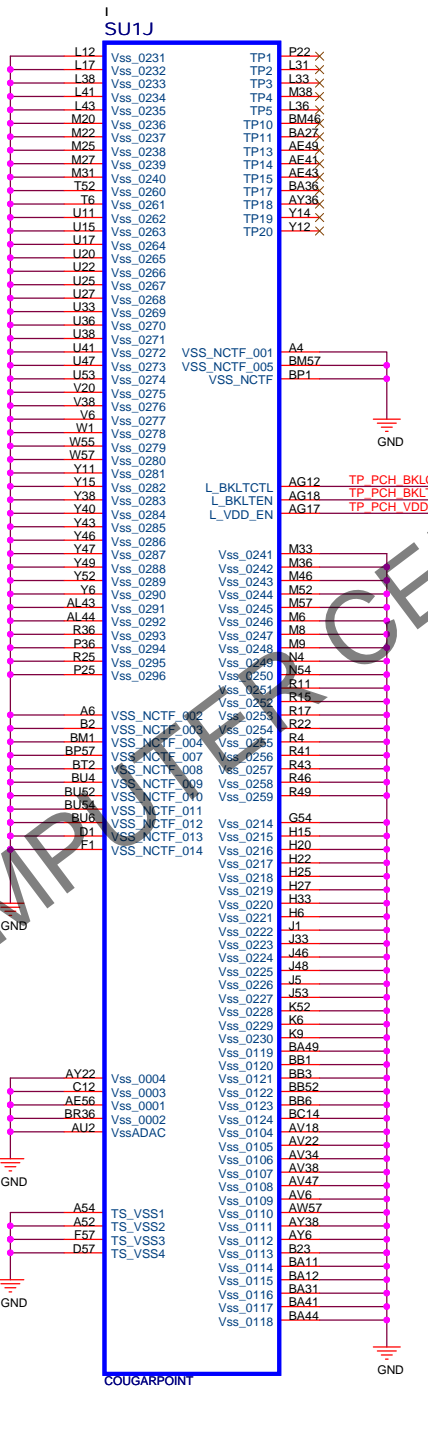
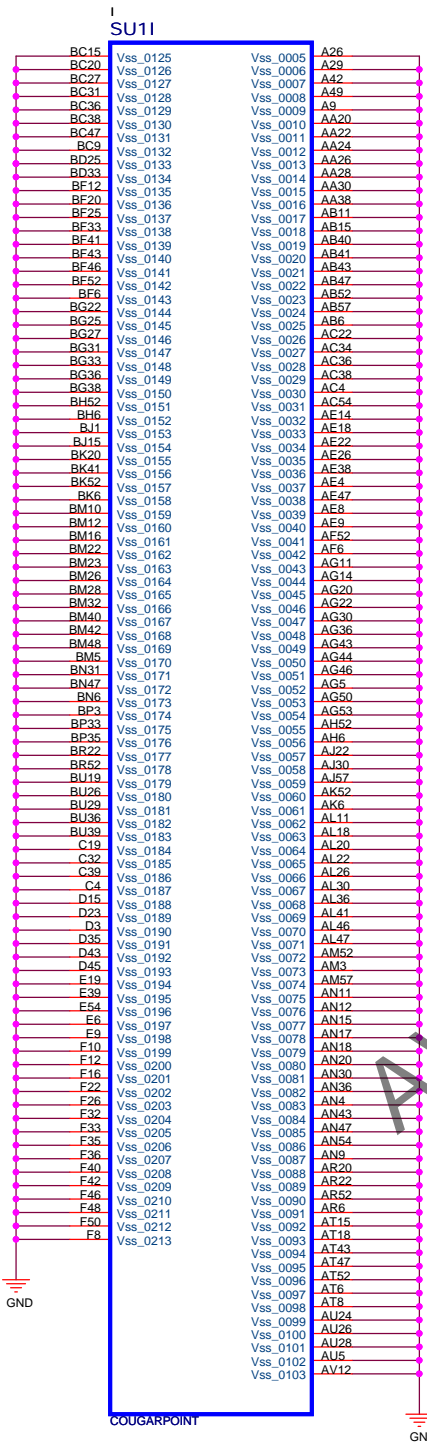


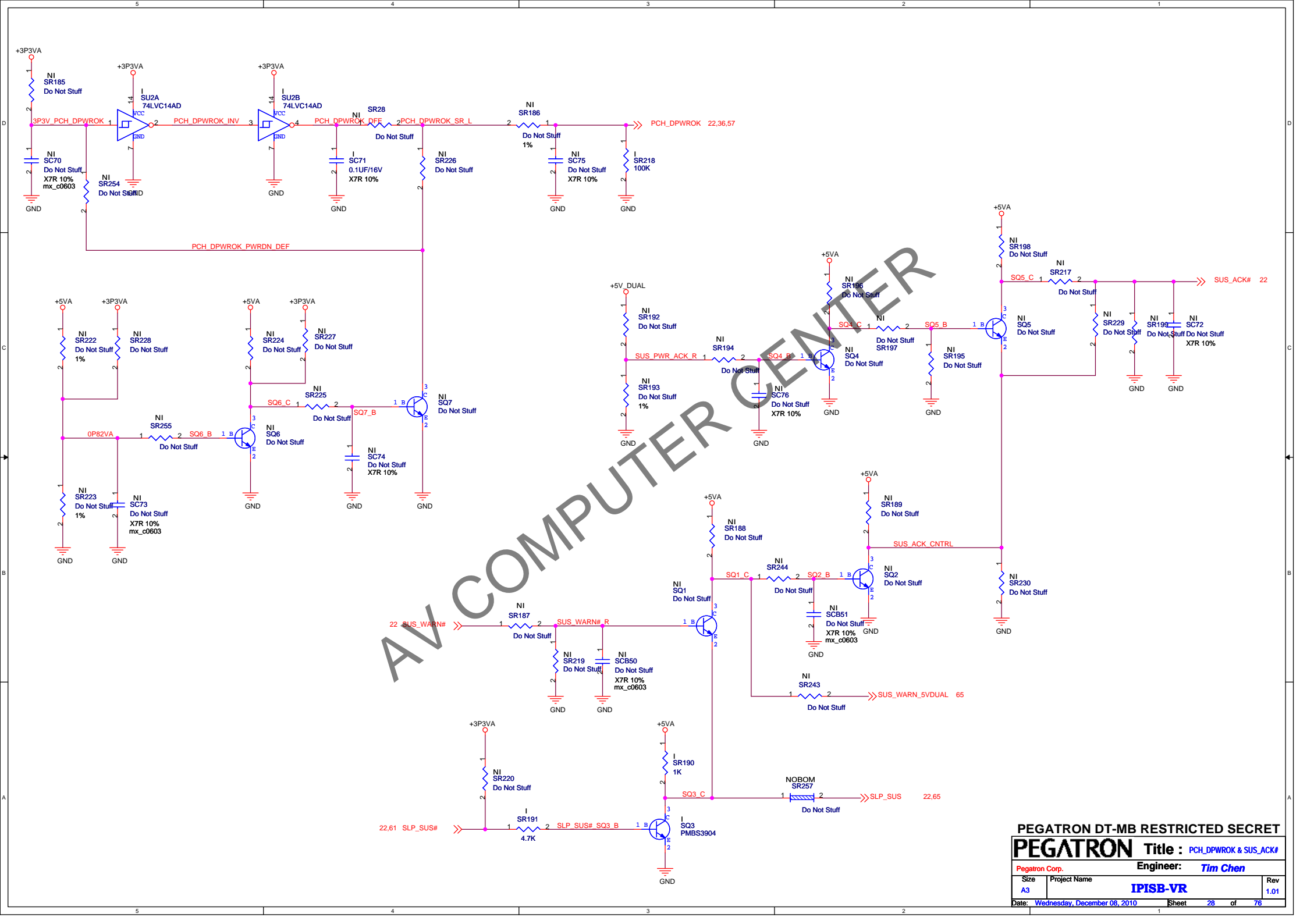












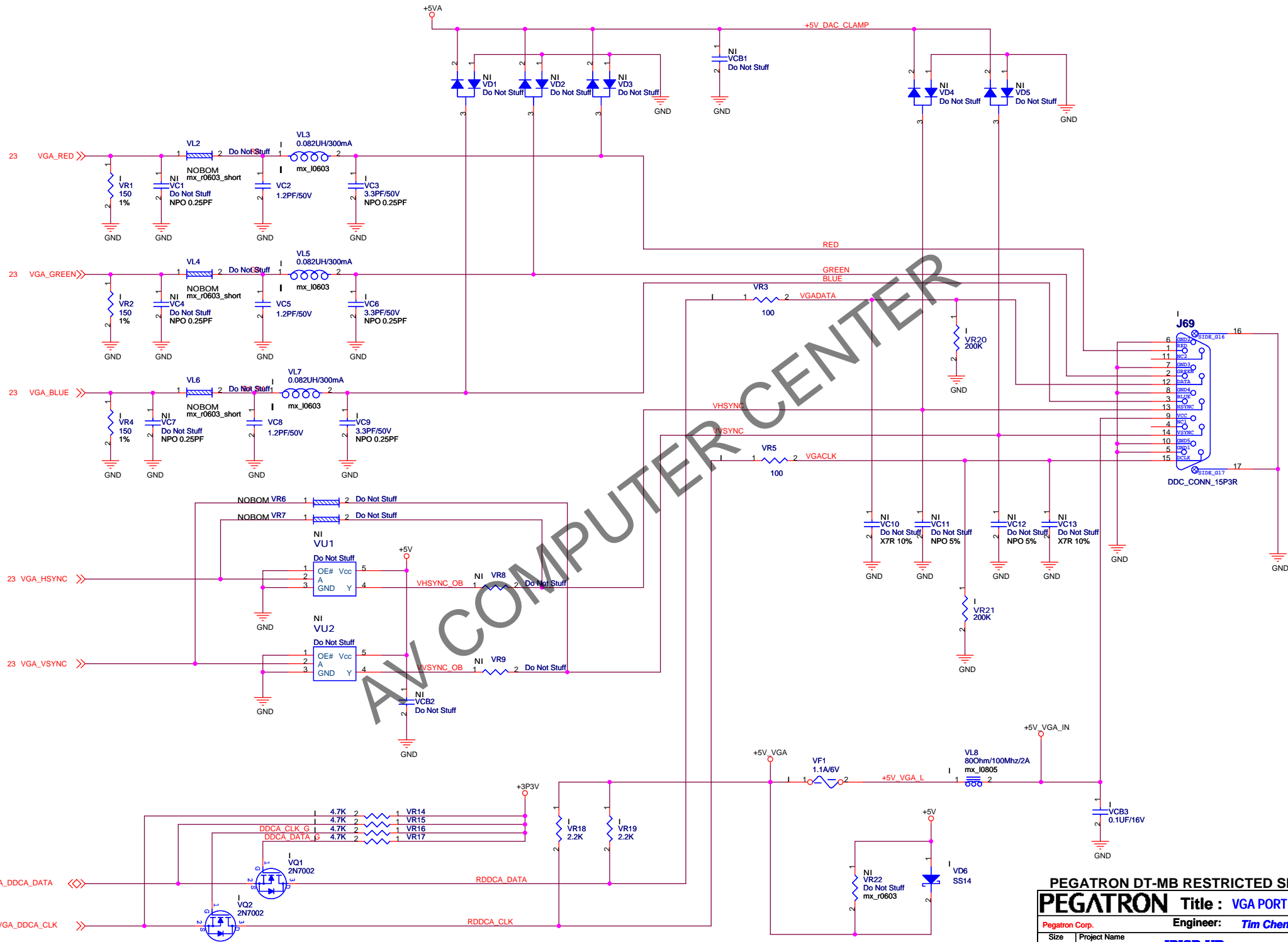
PEGATRON DT-MB RESTRICTED SECRET

**PEGATRON** Title : PCH\_DPWRK & SUS\_ACK#

Pegatron Corp. Engineer: Tim Chen

Size A3	Project Name <b>IPISB-VR</b>	Rev 1.01
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Date: Wednesday, December 08, 2010 Sheet 28 of 76

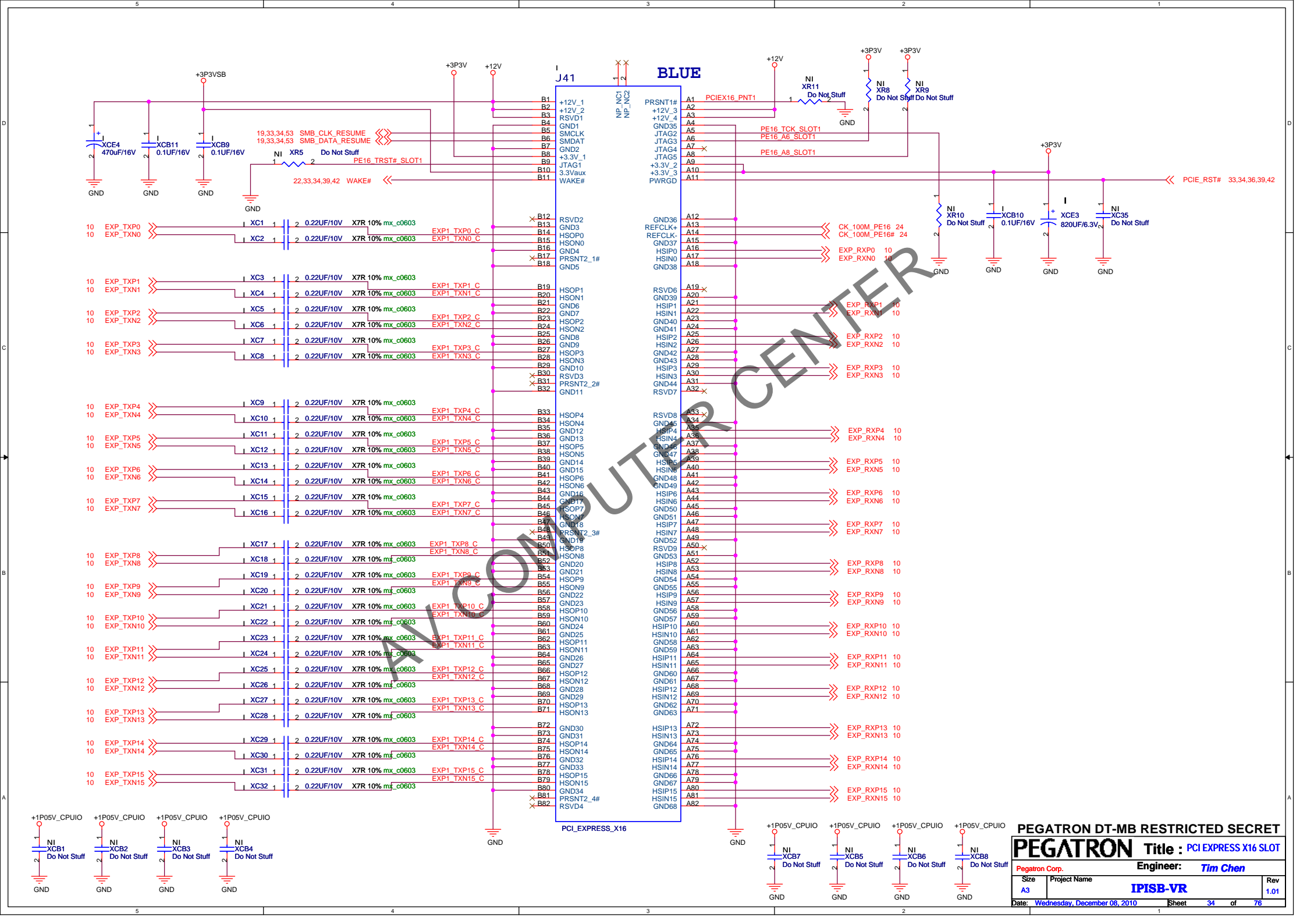


AV COMPUTER CENTER

67-00000-01 PEGATRON DT-MB RESTRICTED SECRET

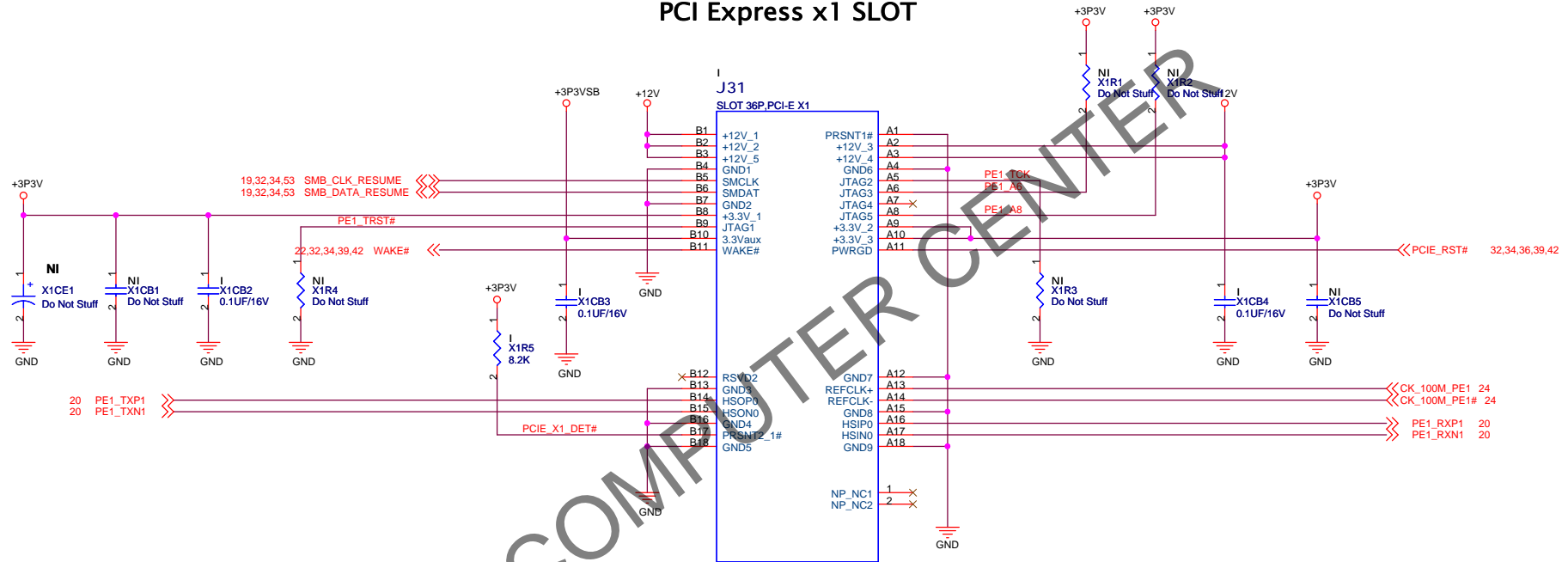
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PEGATRON CORP.			Engineer: <b>Tim Chen</b>	
Size A3	Project Name <b>IPISB-VR</b>			Rev 1.01
Date: <b>Wednesday, December 08, 2010</b>		Sheet <b>30</b> of <b>76</b>		

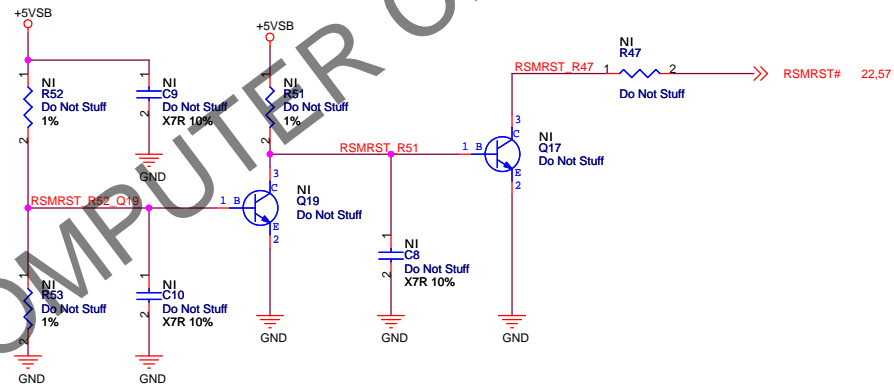
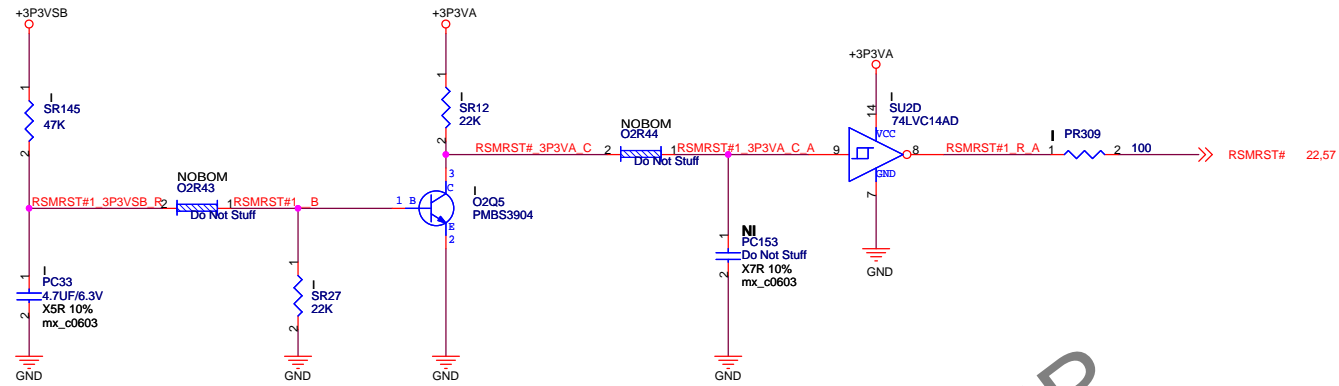






# PCI Express x1 SLOT

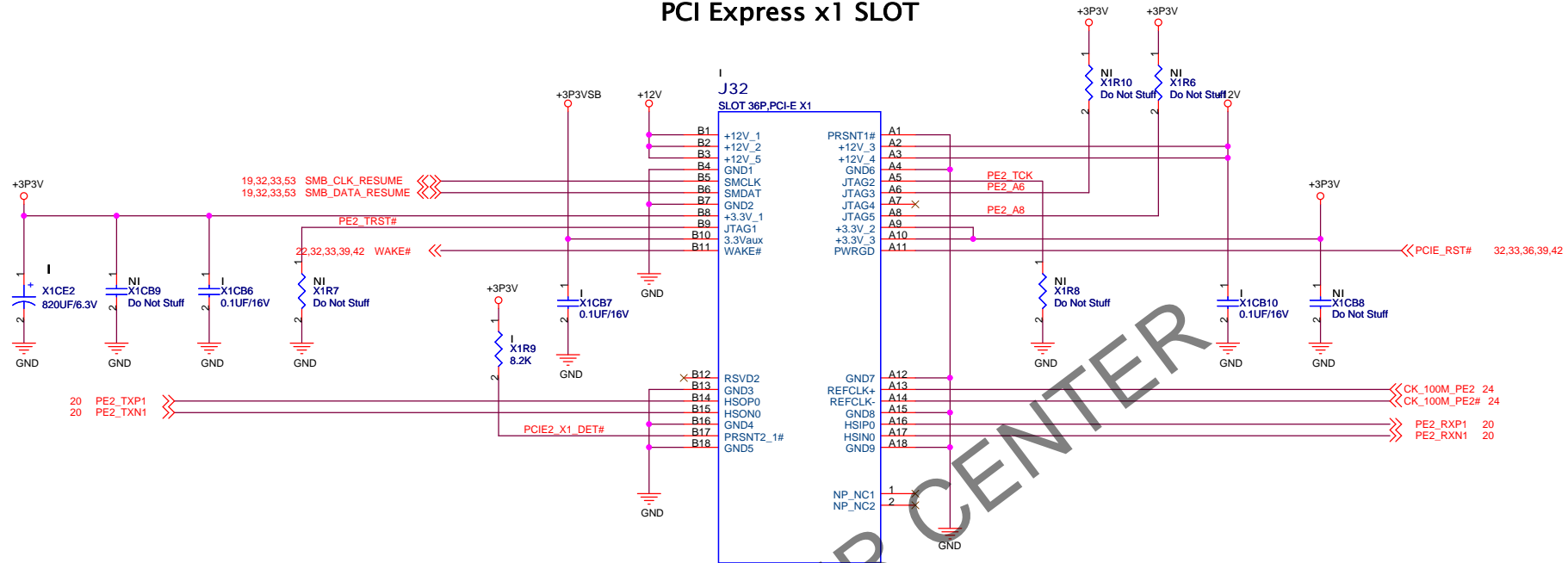




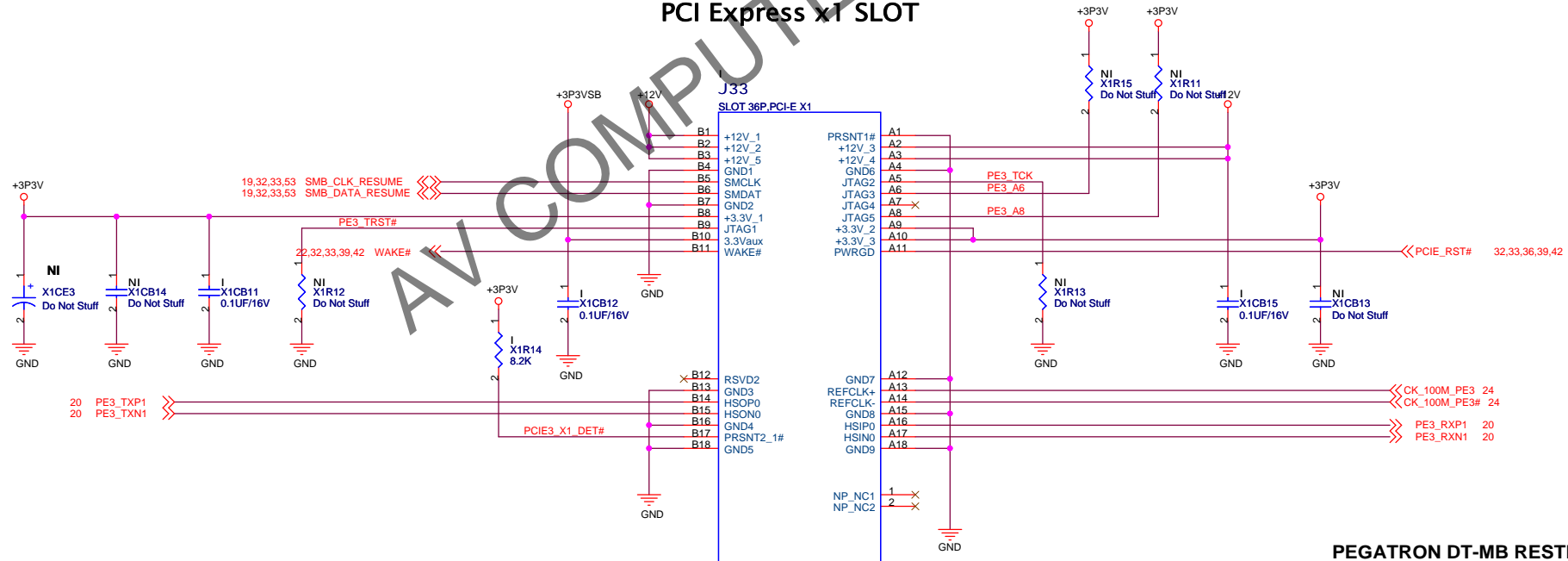
67R101A

<b>PEGATRON</b>		Title : <b>RSMRST CIRCUIT</b>	
<b>PEGATRON CORP.</b>		Engineer: <b>Tim Chen</b>	
Size <b>A3</b>	Project Name <b>IPISB-VR</b>		Rev <b>1.01</b>
Date: <b>Wednesday, December 08, 2010</b>		Sheet <b>35</b>	of <b>76</b>

## PCI Express x1 SLOT



## PCI Express x1 SLOT



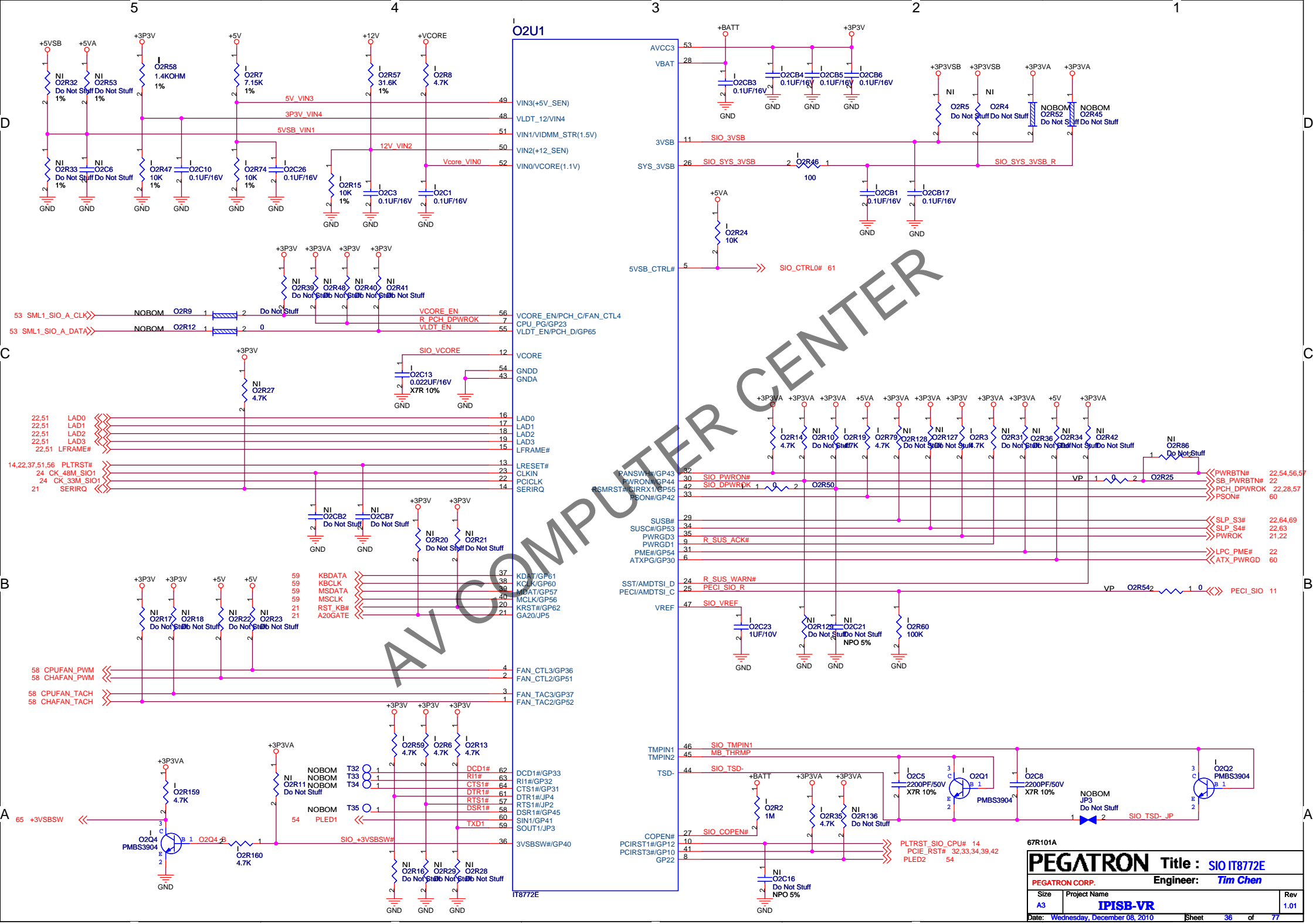
PEGATRON DT-MB RESTRICTED SECRET

**PEGATRON** Title : PCI SLOT

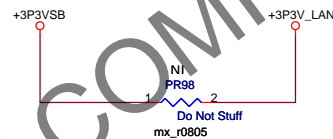
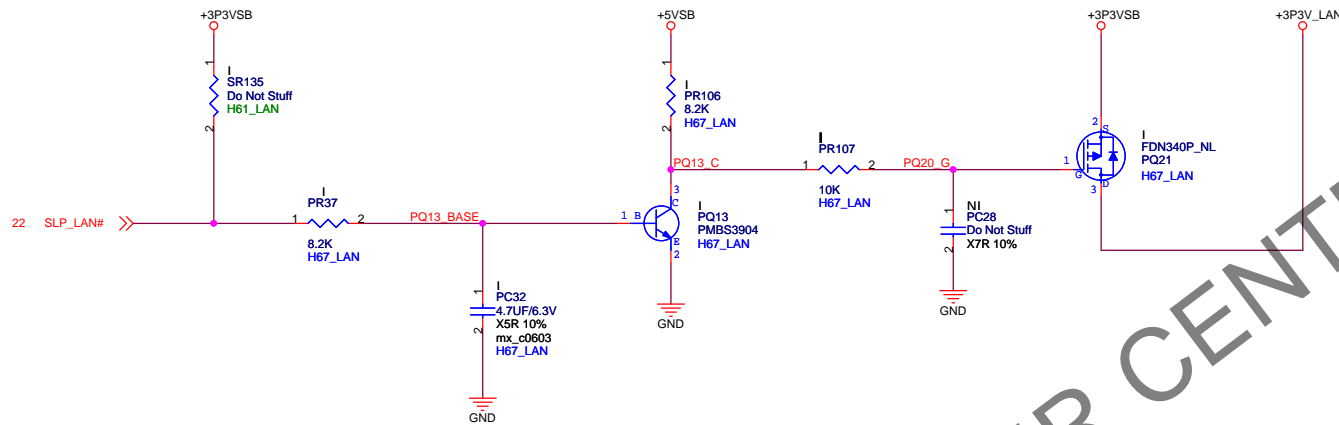
Pegatron Corp. Engineer: Tim Chen

Size A3 Project Name IPISB-VR Rev 1.01

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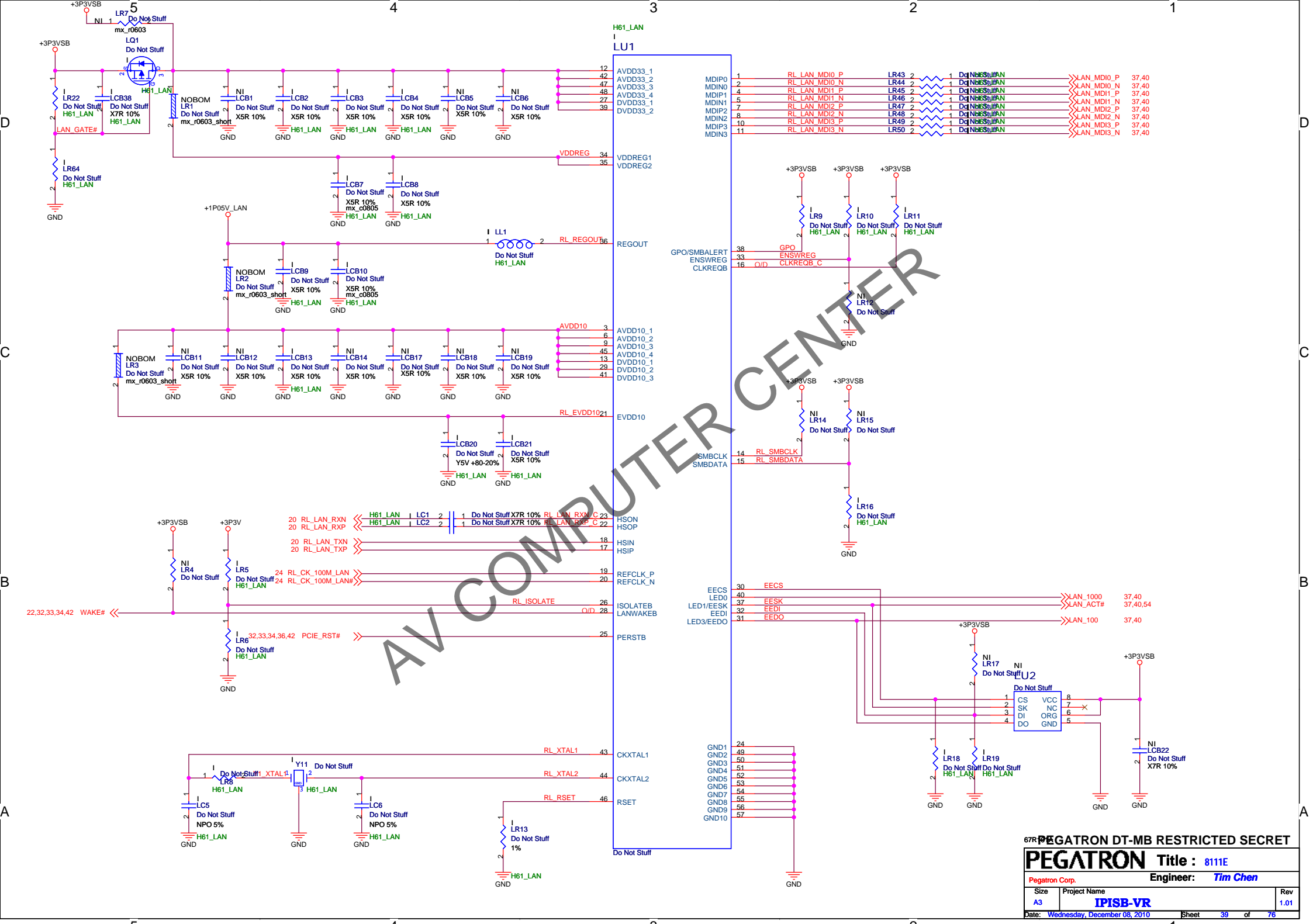
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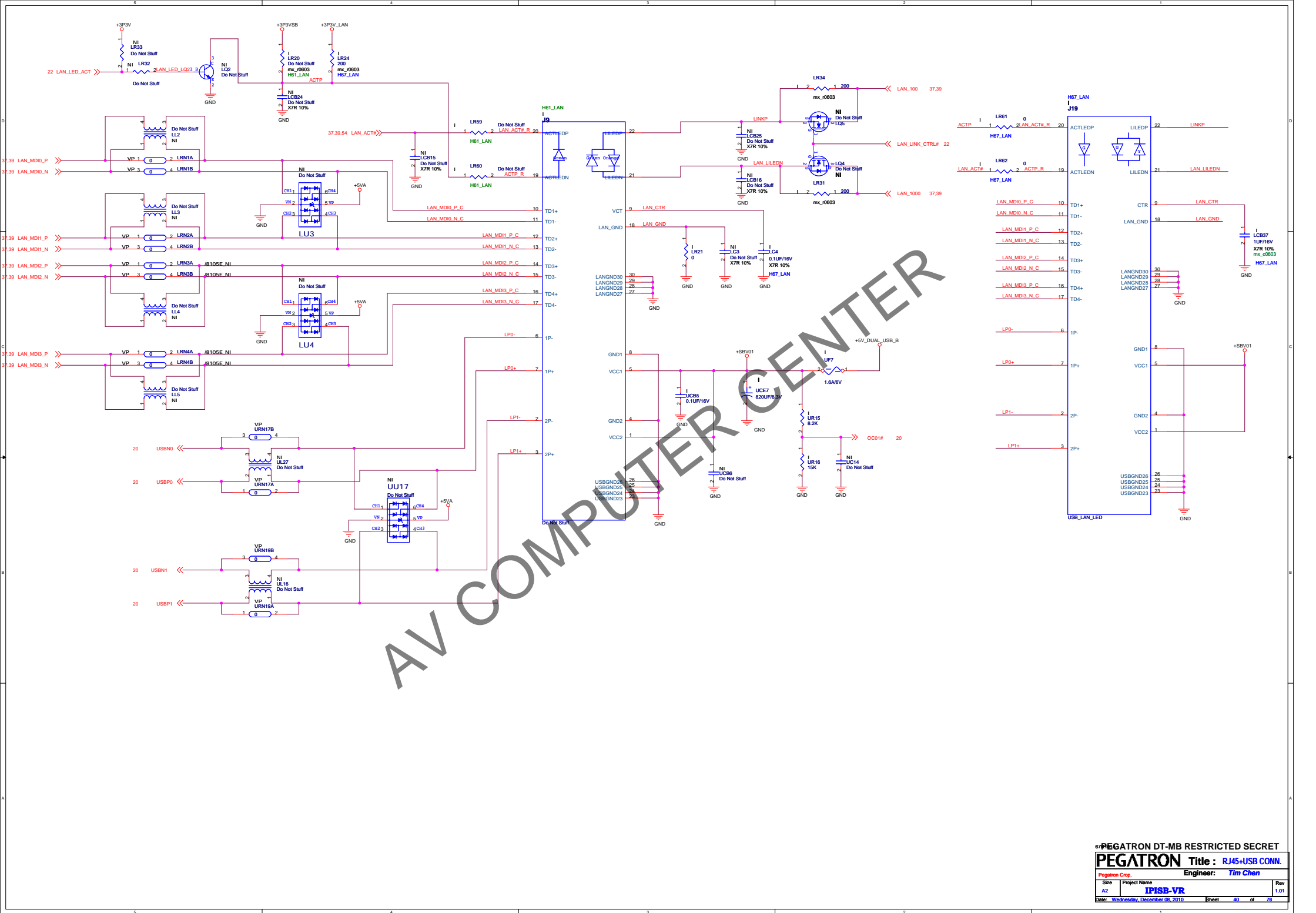
**PEGATRON** Title : HDMI CONNECTOR

Pegatron Corp. Engineer: Tim Chen

Size A3	Project Name <b>IPISB-VR</b>	Rev 1.01
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Date: Wednesday, December 08, 2010 Sheet 38 of 76





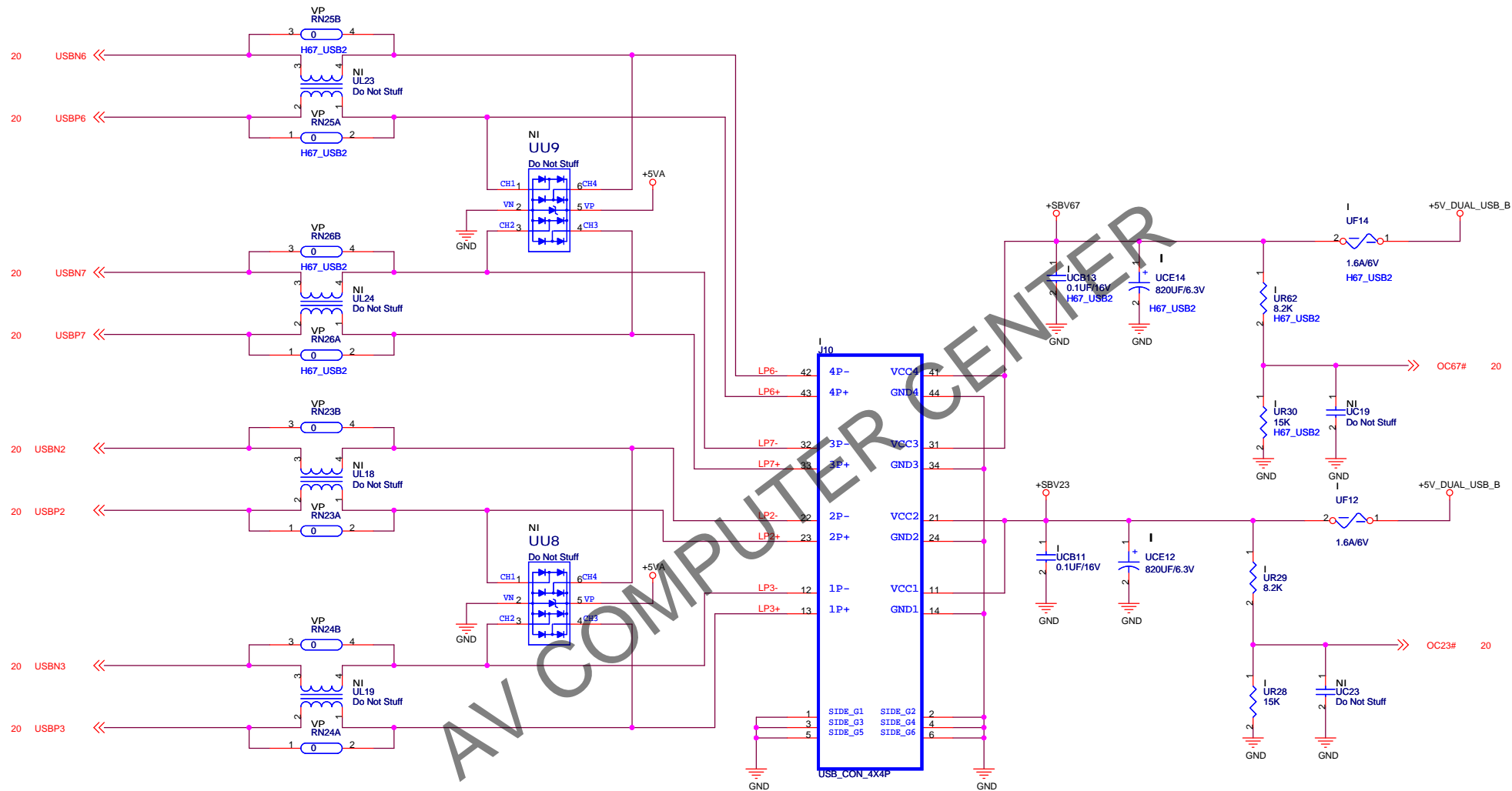
67 PEGATRON DT-MB RESTRICTED SECRET

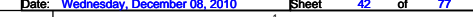
**PEGATRON** Title : RJ45+USB CONN.

Size Project Name Engineer: Tim Chen

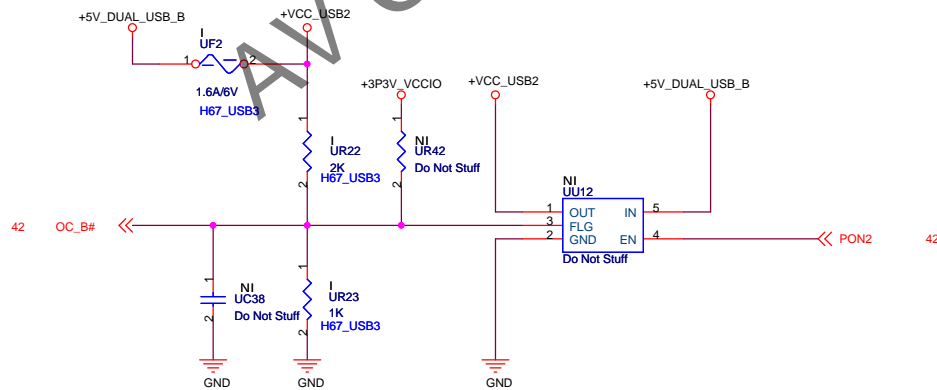
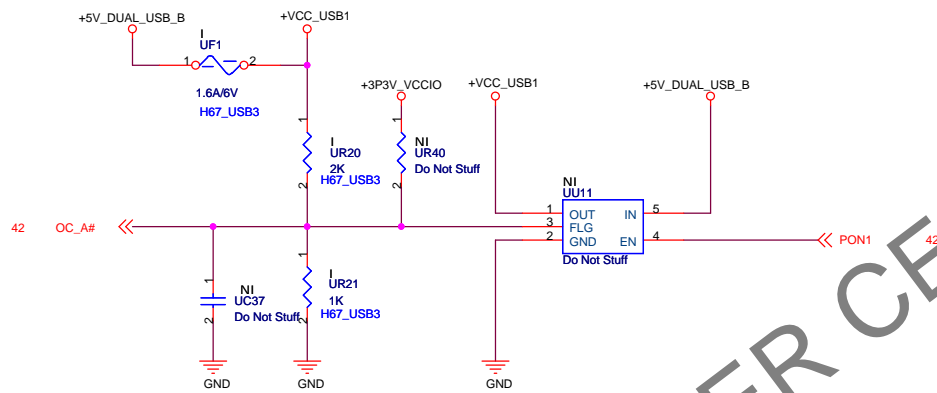
Date: Wednesday, December 08, 2010 Sheet 40 of 76

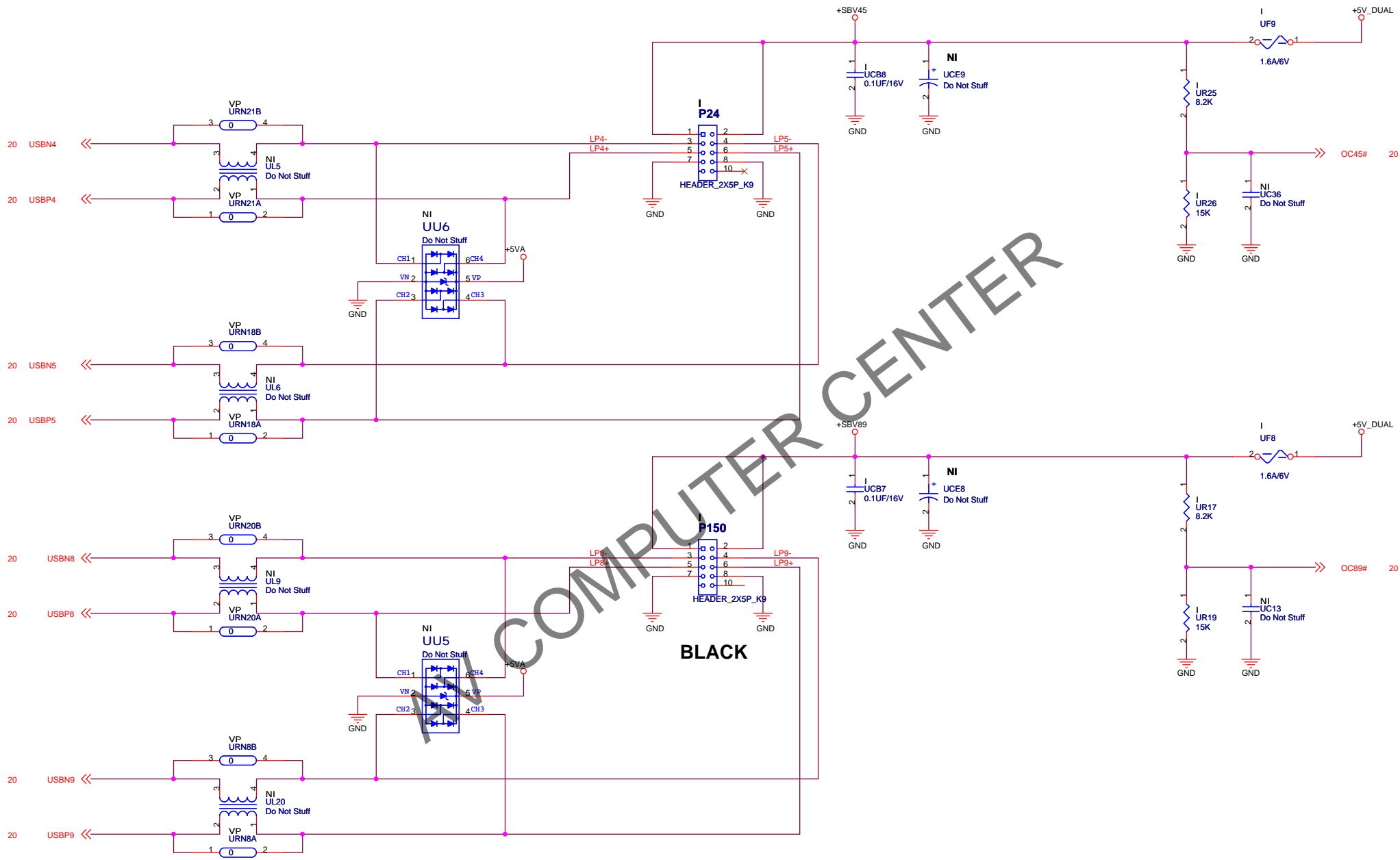


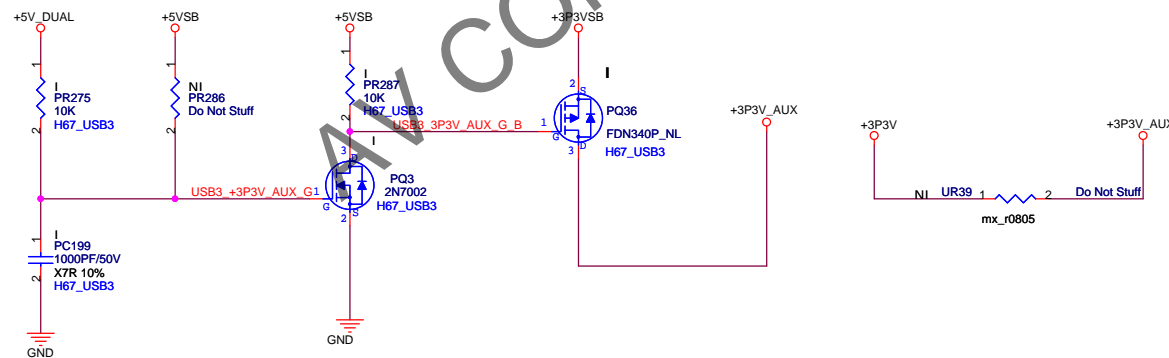
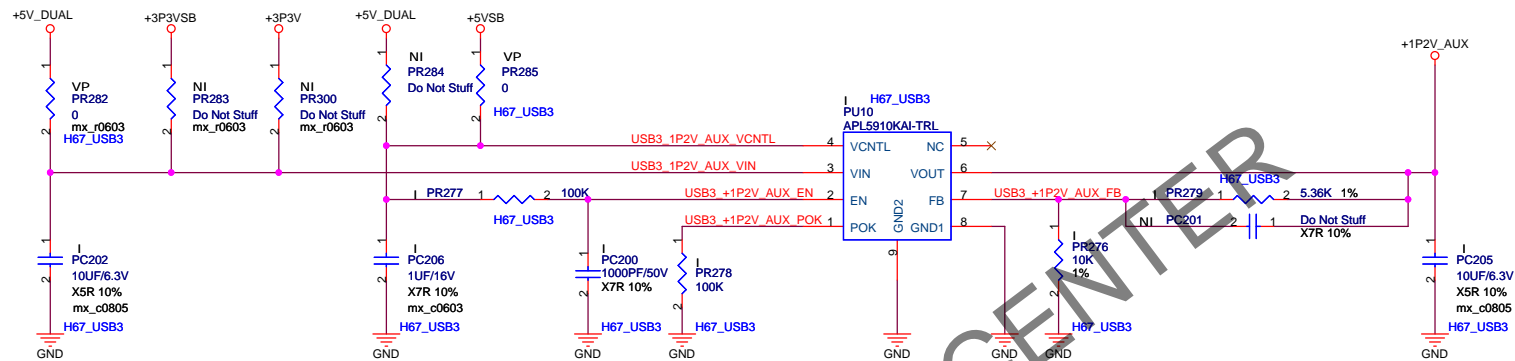












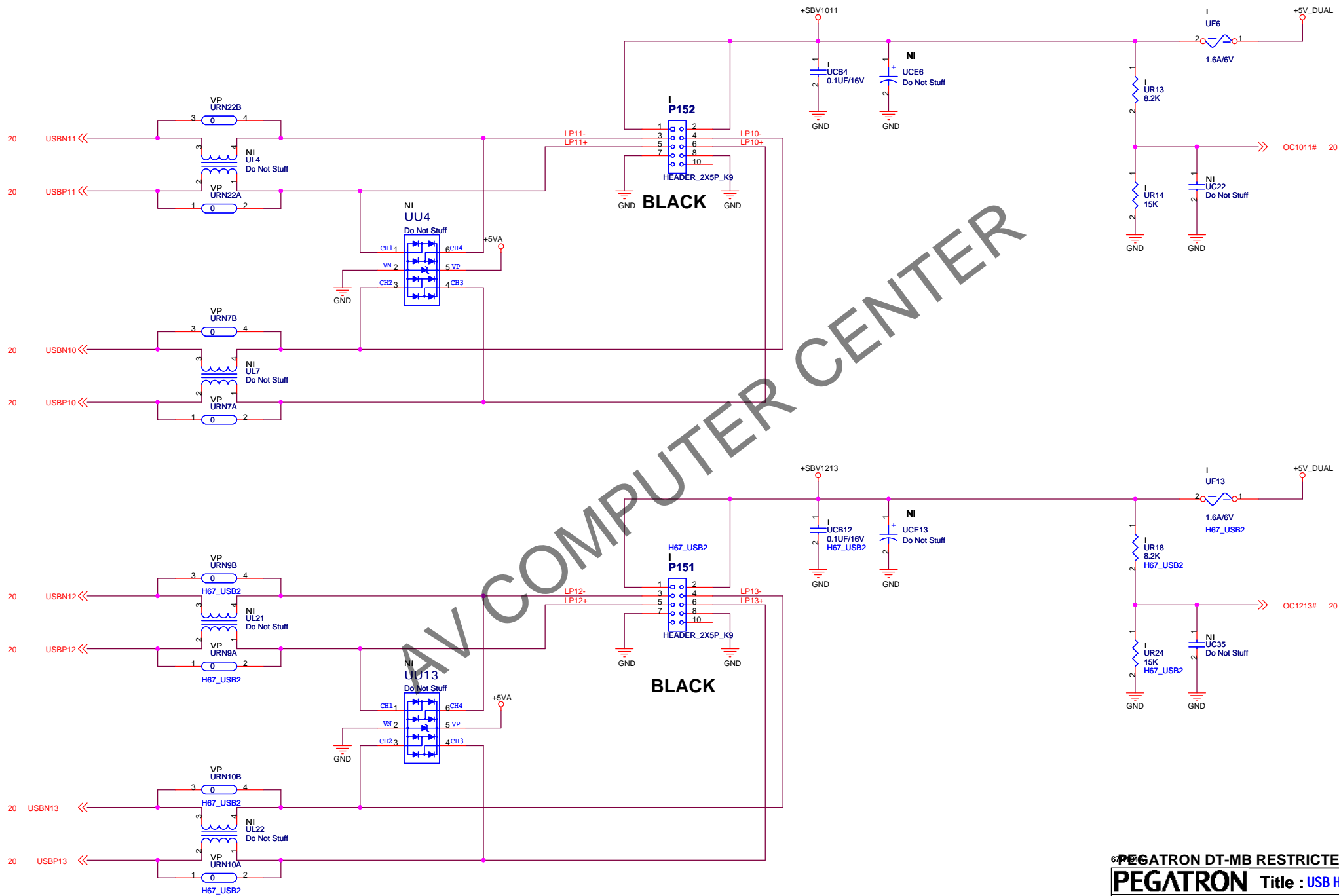
PEGATRON DT-MB RESTRICTED SECRET

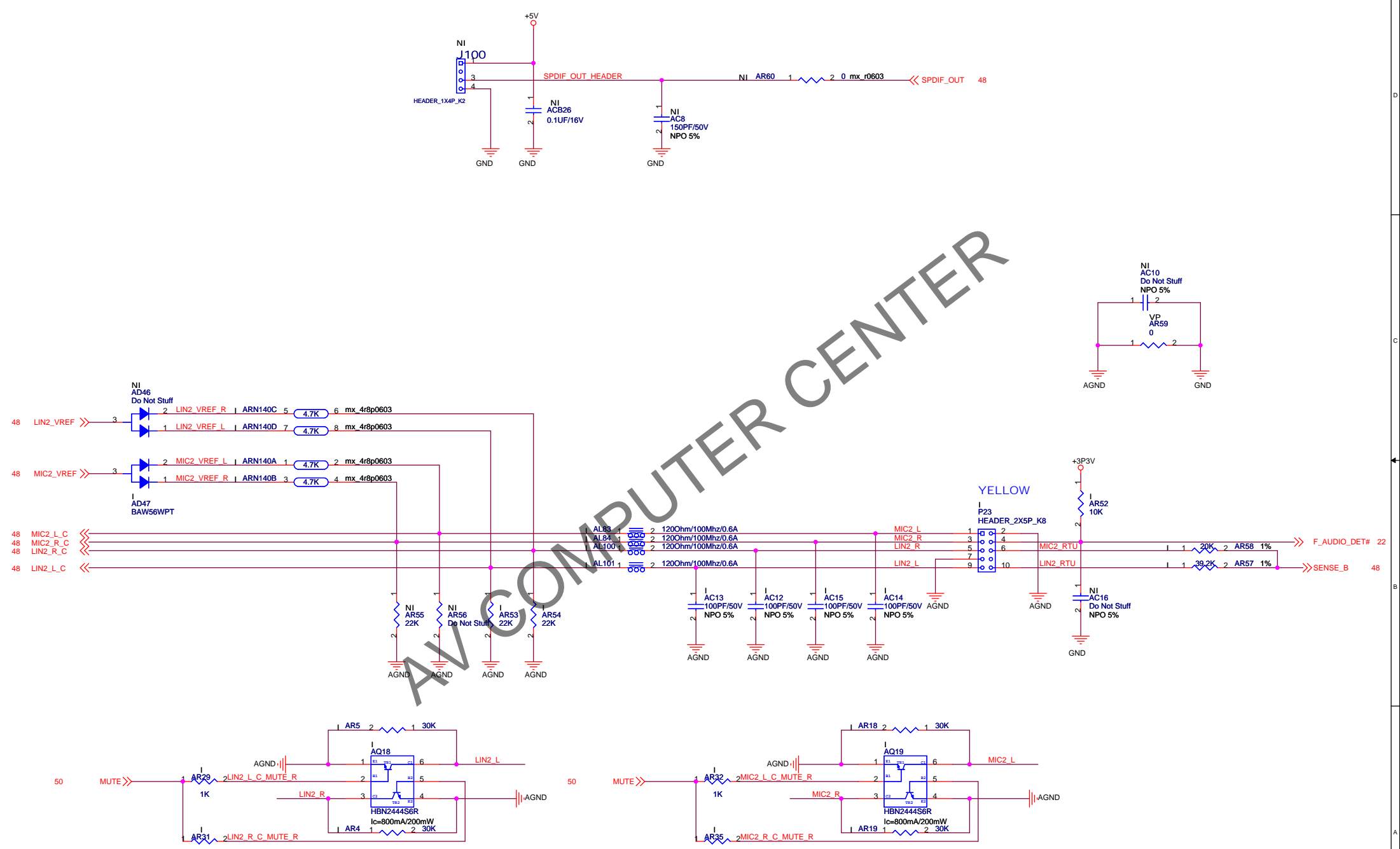
**PEGATRON** Title : **USB POWER**

PEGATRON CORPORATION Engineer: **Tim Chen**

Size A3	Project Name <b>IPISB-VR</b>	Rev 1.01
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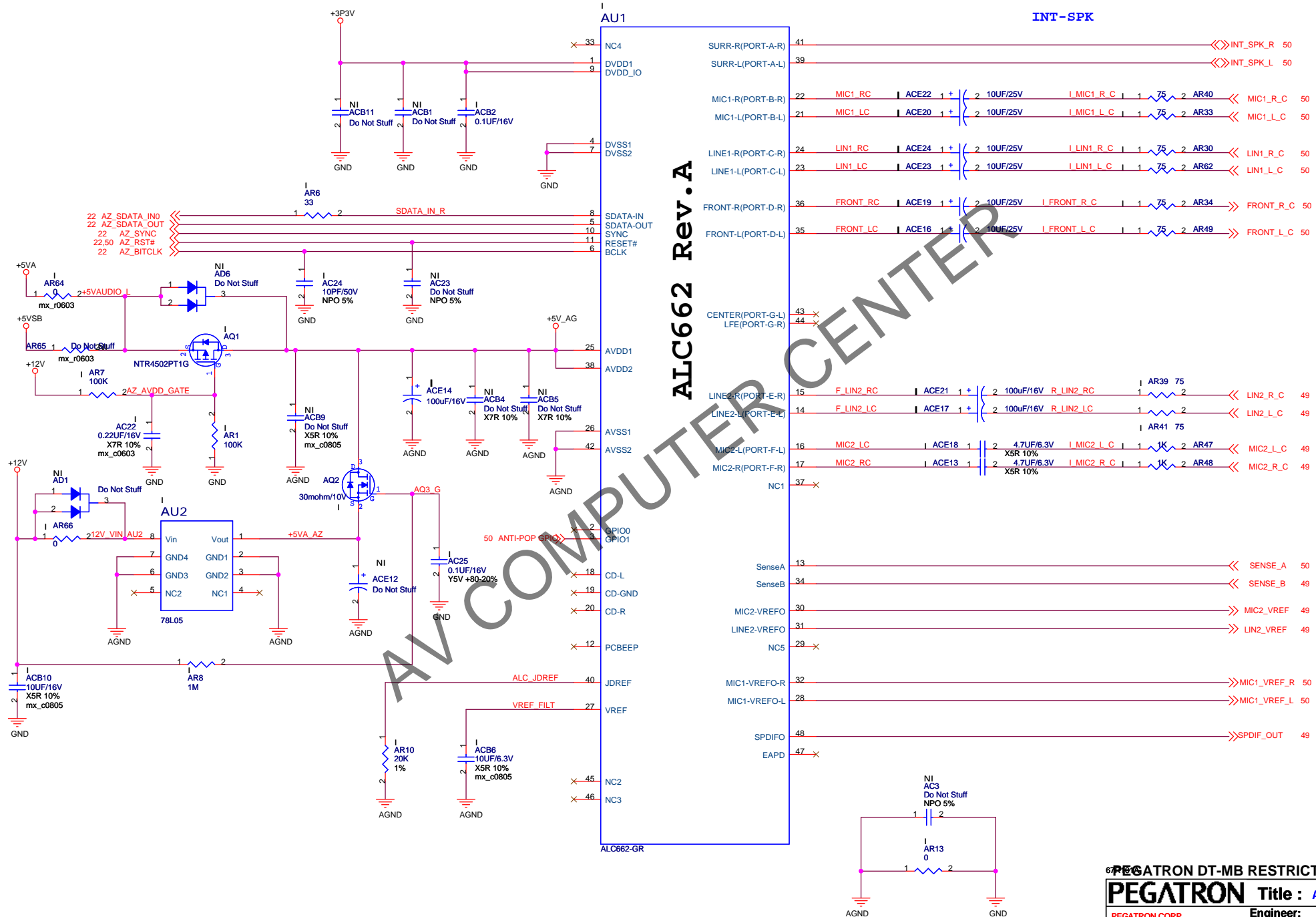
Date: Wednesday, December 08, 2010 Sheet 45 of 76







ALC662 Rev.A



PEGATRON DT-MB RESTRICTED SECRET

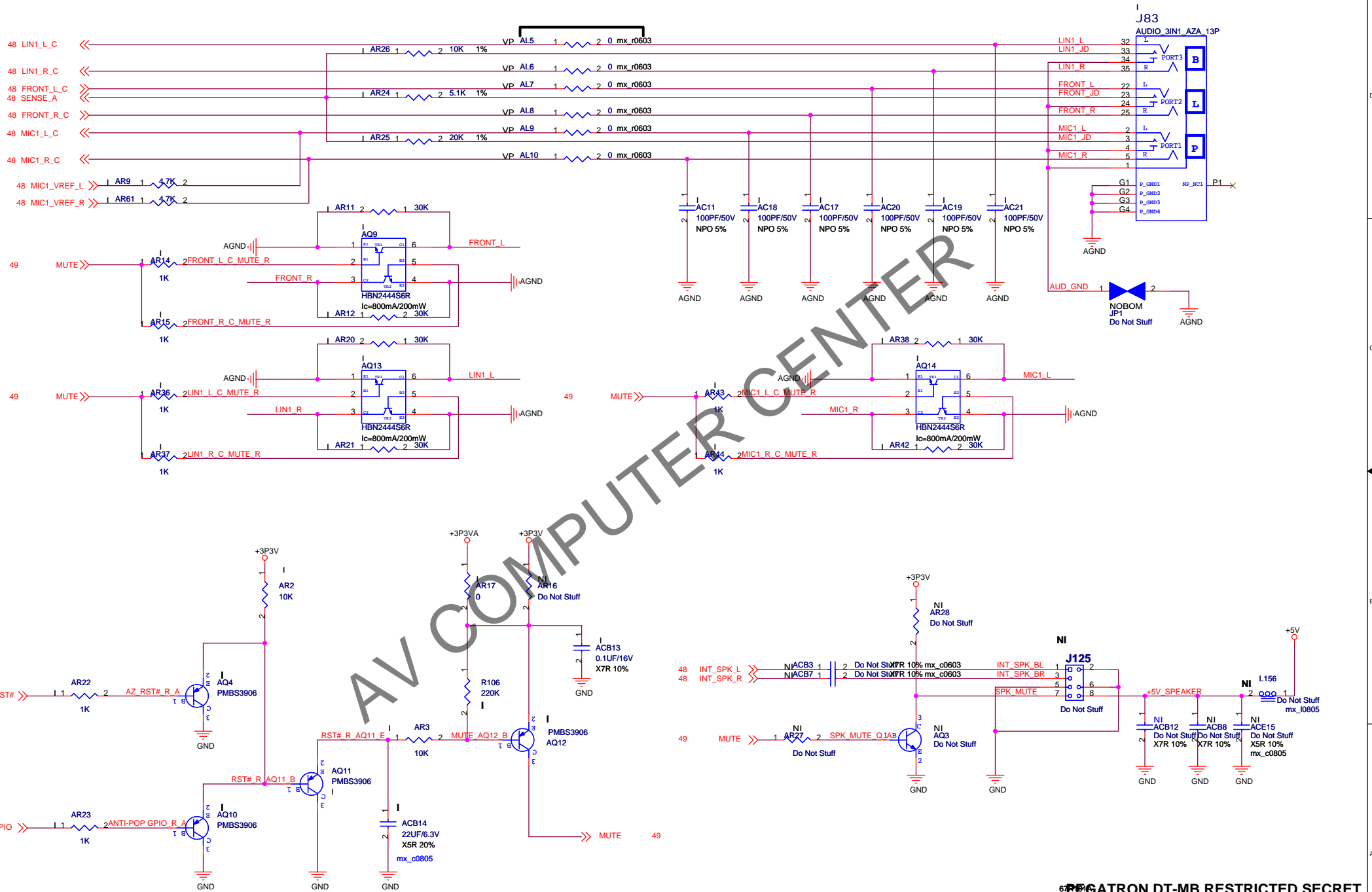
PEGATRON Title : ALC662 CODEC

PEGATRON CORP. Engineer: Tim Chen

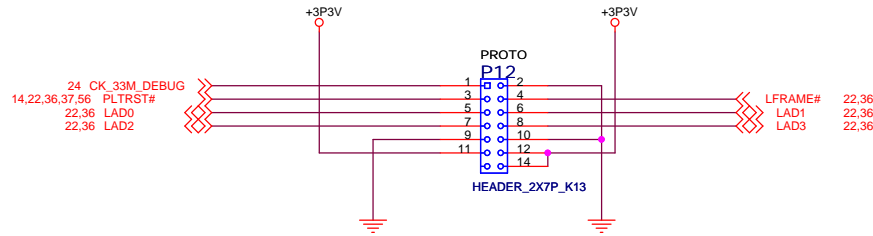
Size A3 Project Name IPISE-VR

Date: Wednesday, December 08, 2010 Sheet 48 of 76

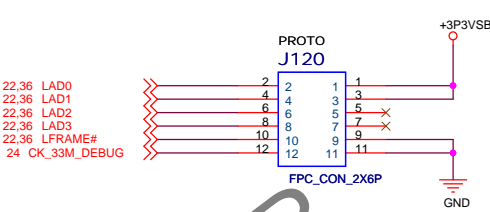
Rev 1.01



LPC DEBUG1

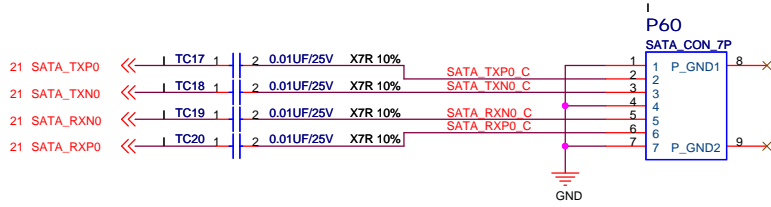


LPC DEBUG2

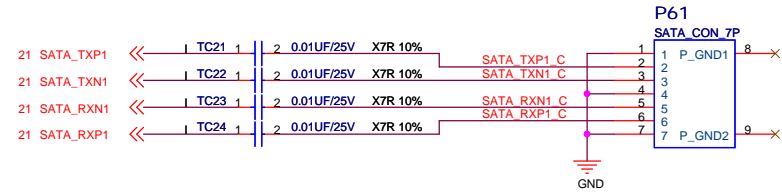


AV COMPUTER CENTER

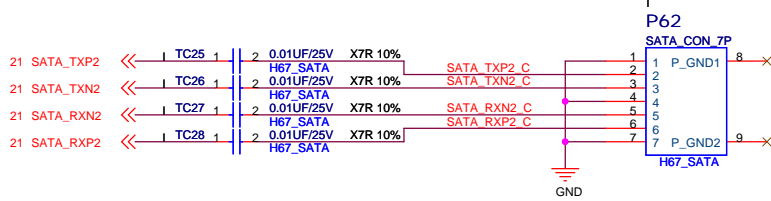
# SATA CONNECTOR



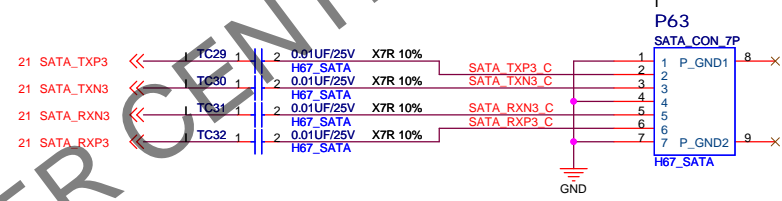
**SATA CONTROLLER #1  
(PRIMARY MASTER)**  
COLOR = BLACK



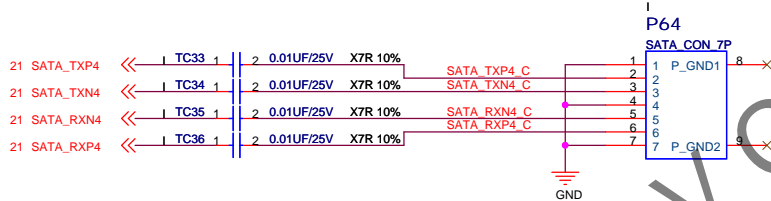
**SATA CONTROLLER #1  
(SECONDARY MASTER)**  
COLOR = BLACK



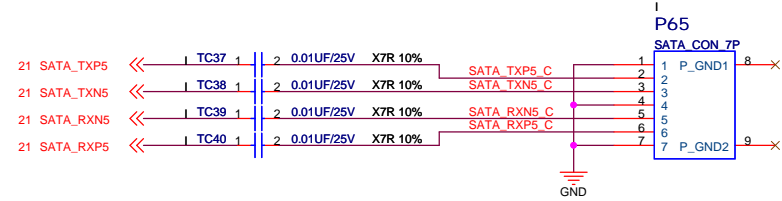
**SATA CONTROLLER #2  
(PRIMARY MASTER)**  
COLOR = BLACK



**SATA CONTROLLER #2  
(SECONDARY MASTER)**  
COLOR = BLACK



**SATA CONTROLLER #3  
(PRIMARY MASTER)**  
COLOR = BLACK



**SATA CONTROLLER #3  
(SECONDARY MASTER)**  
COLOR = BLACK

PEGATRON DT-MB RESTRICTED SECRET

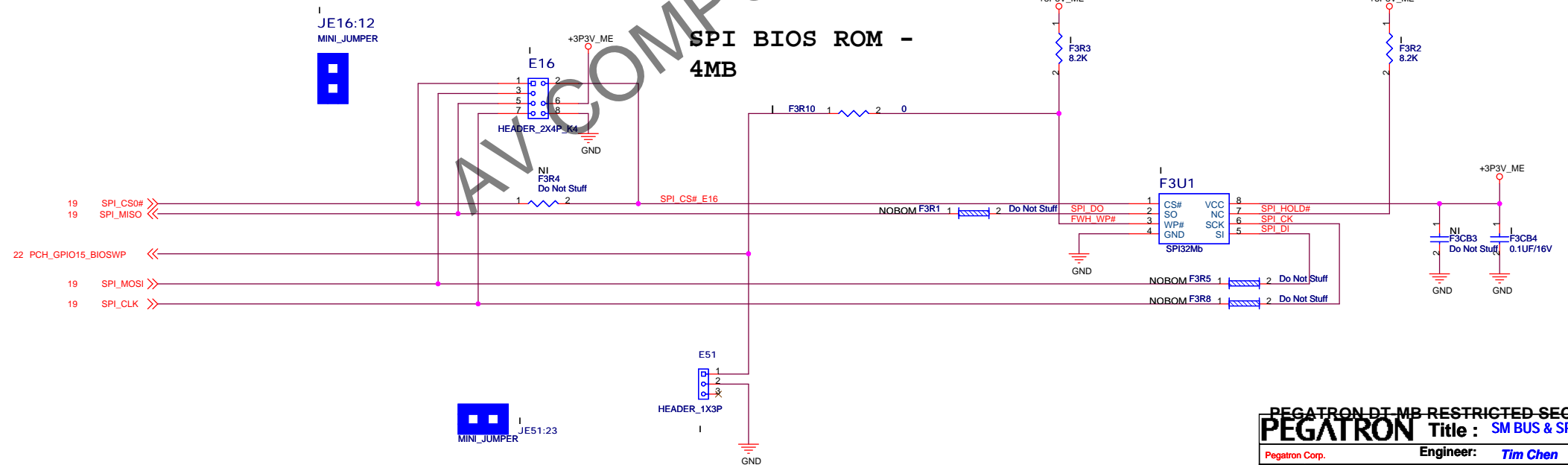
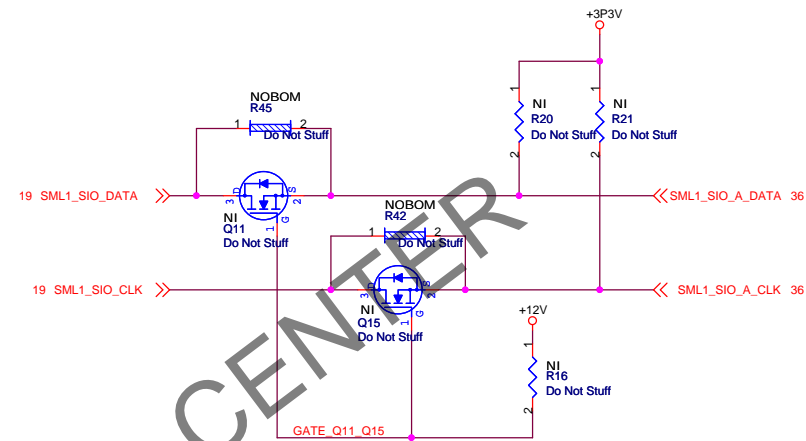
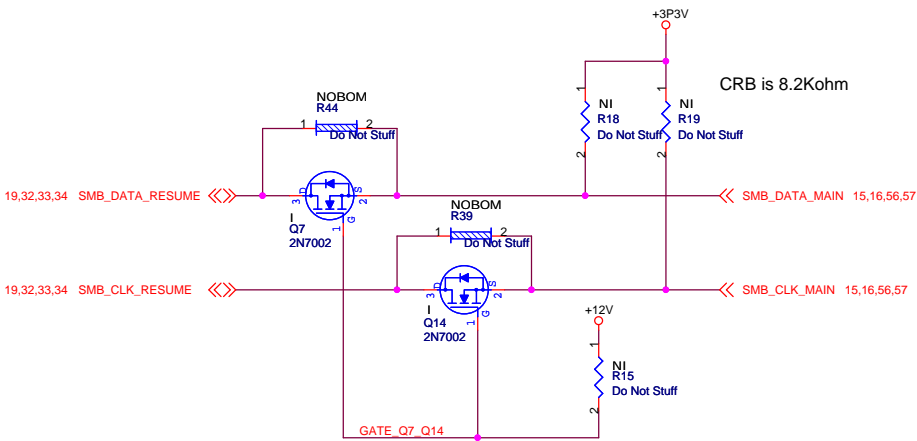
**PEGATRON** Title : **SATA CONNECTOR**

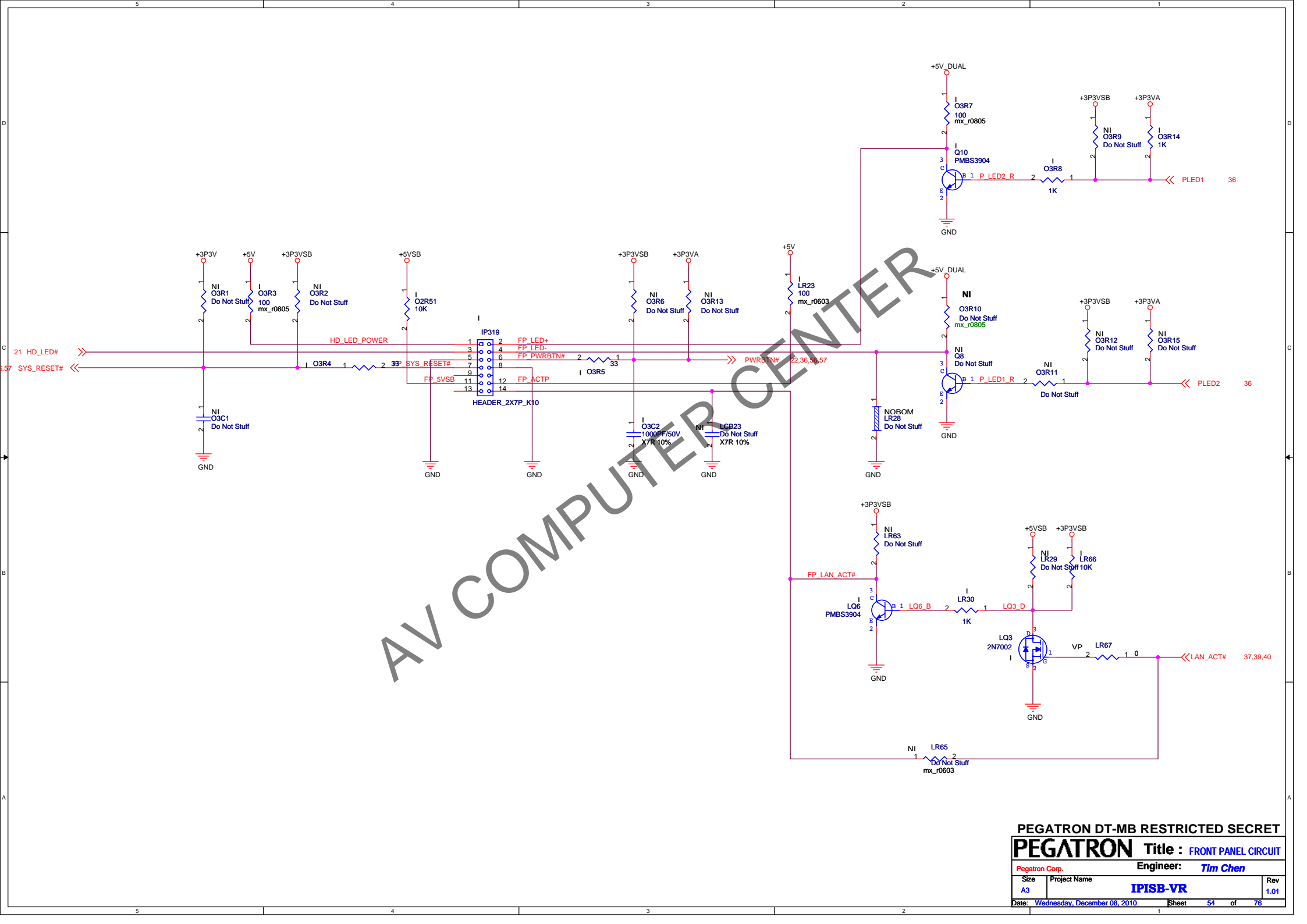
Pegatron Corp. Engineer: **Tim Chen**

Size A3 Project Name **IPISB-VR** Rev 1.01

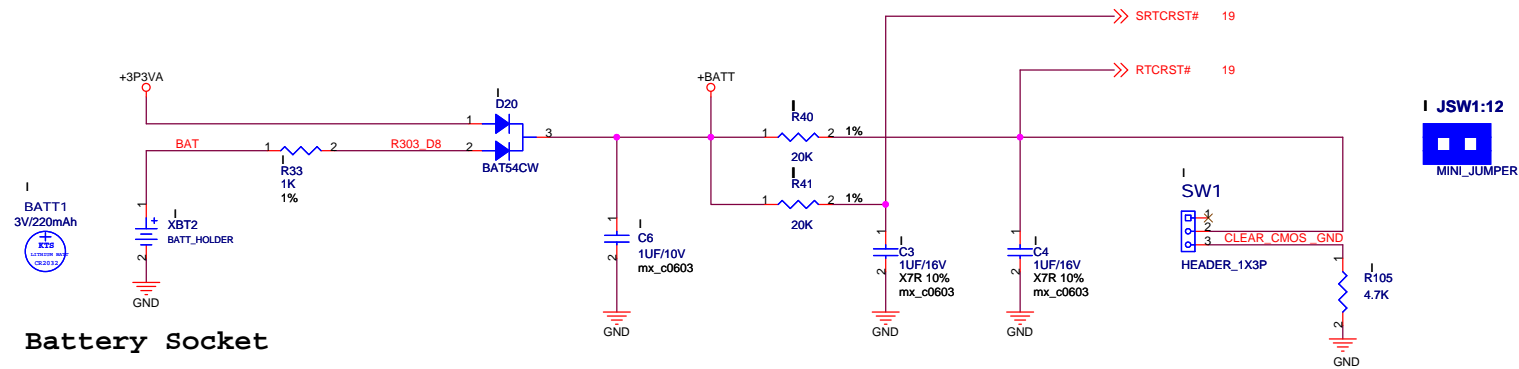
Date: Wednesday, December 08, 2010 Sheet 52 of 76

# SM BUS Control

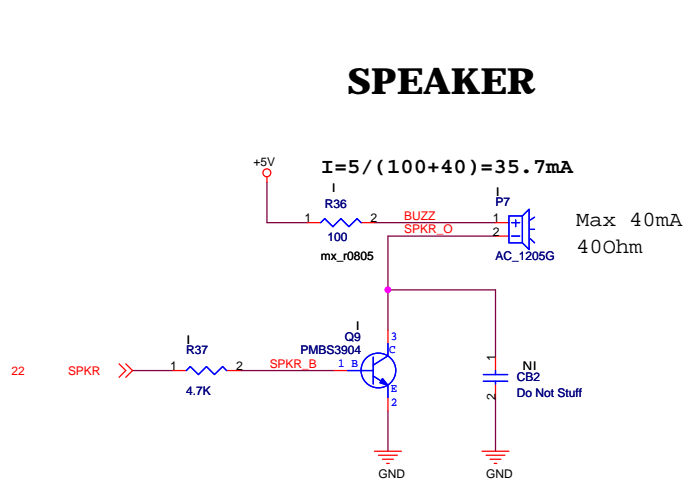




## Battery Socket

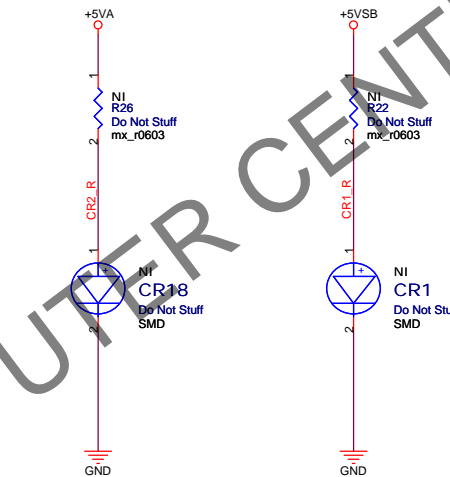


## SPEAKER

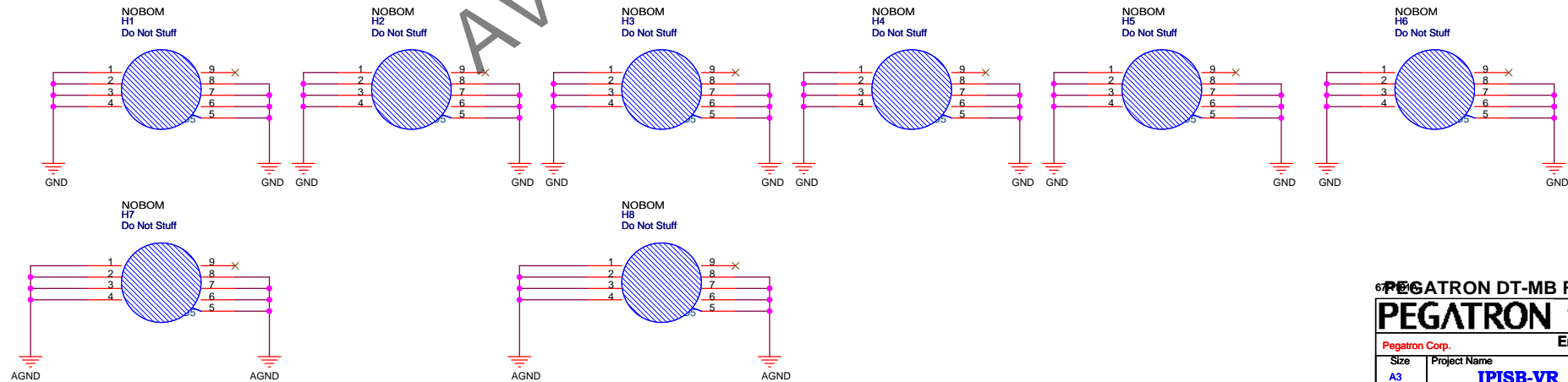
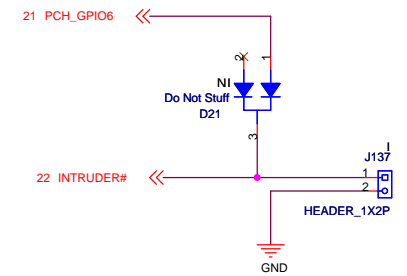


+5VA : BLUE

+5VSB : GREEN



## Case Open



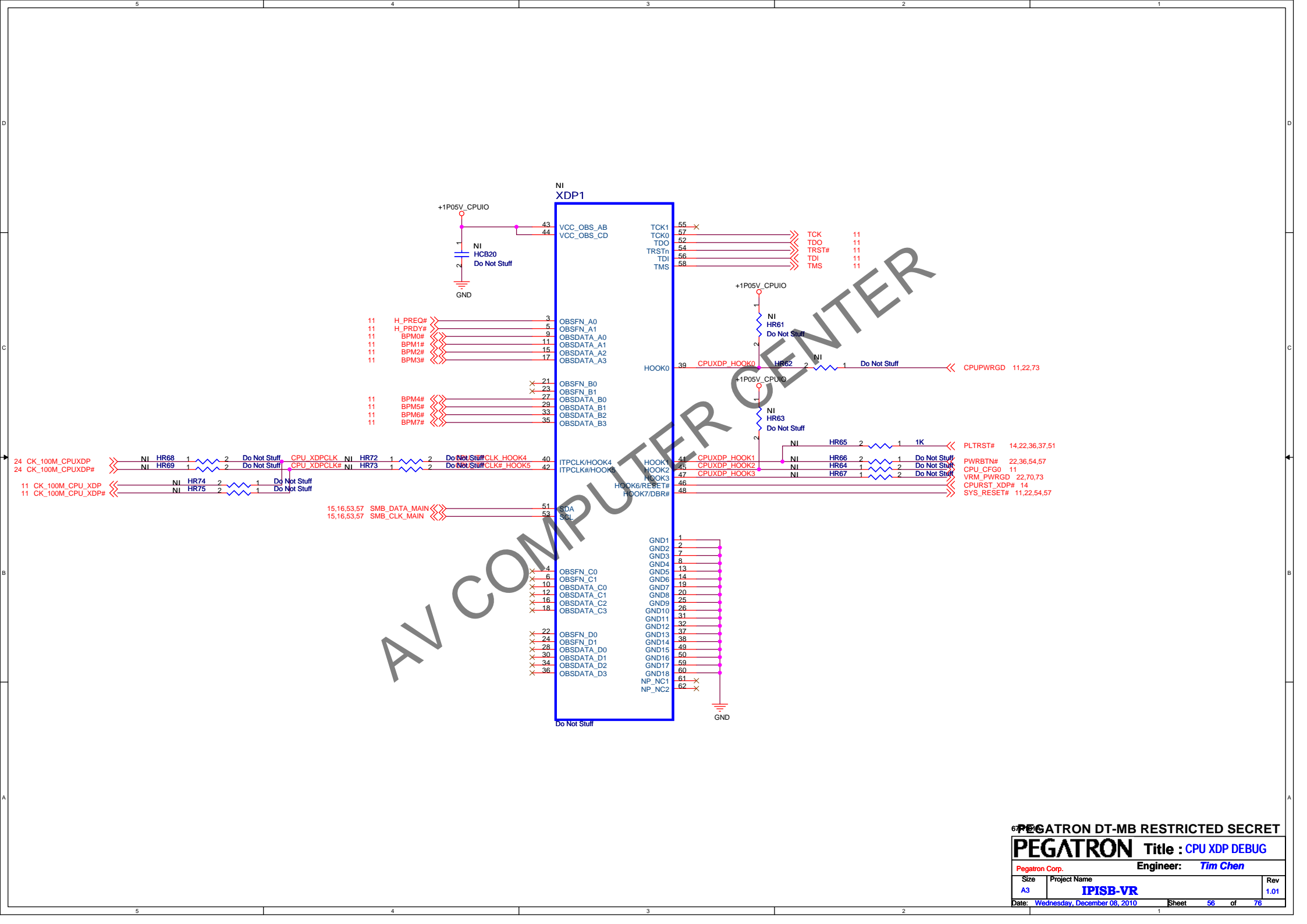
PEGATRON DT-MB RESTRICTED SECRET

PEGATRON Title: PCH/COMS/SPKR/SCREW

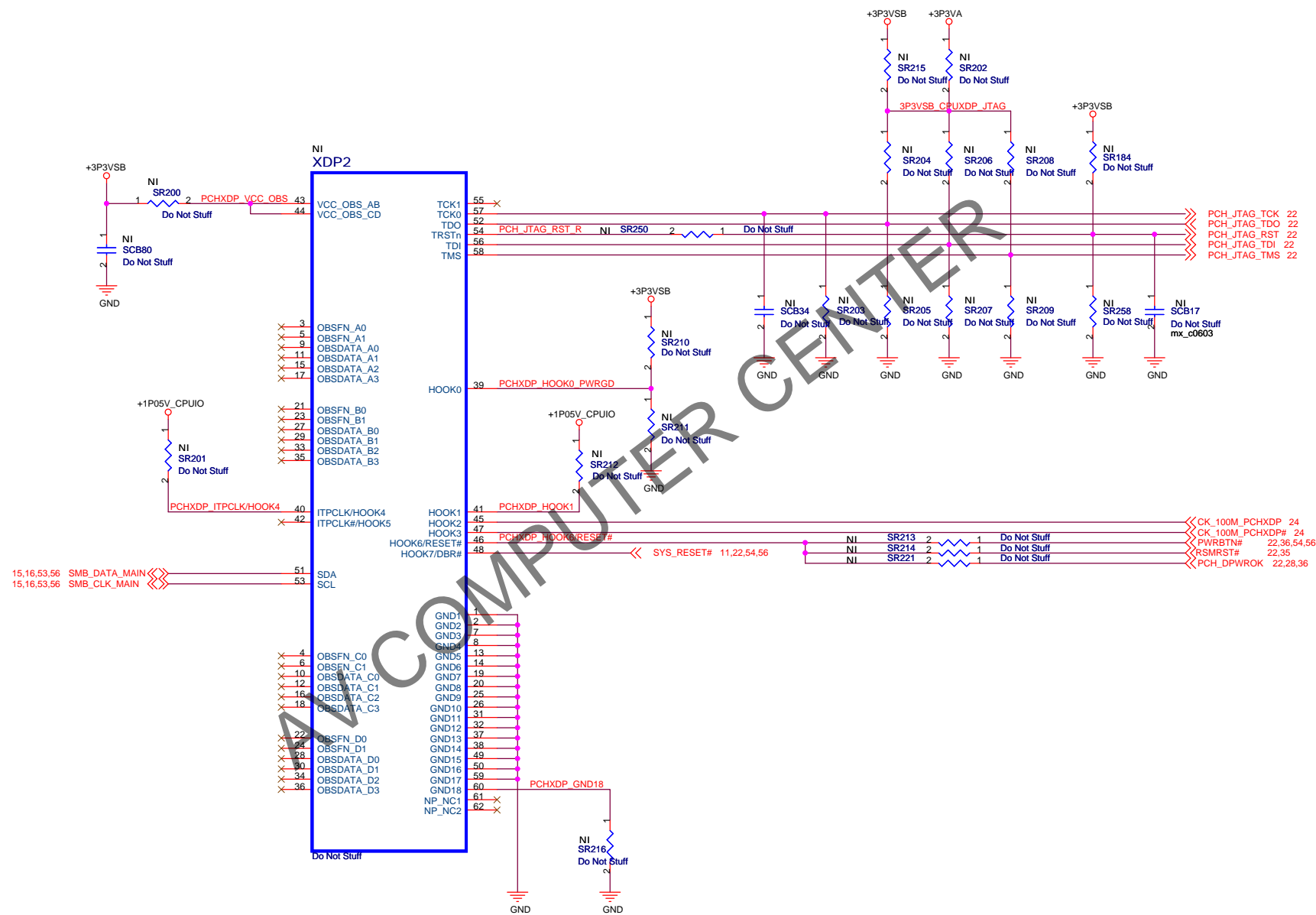
Pegatron Corp. Engineer: Tim Chen

Size A3 Project Name IPISB-VR Rev 1.01

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PEGATRON DT-MB RESTRICTED SECRET

**PEGATRON** Title : PCH XDP DEBUG

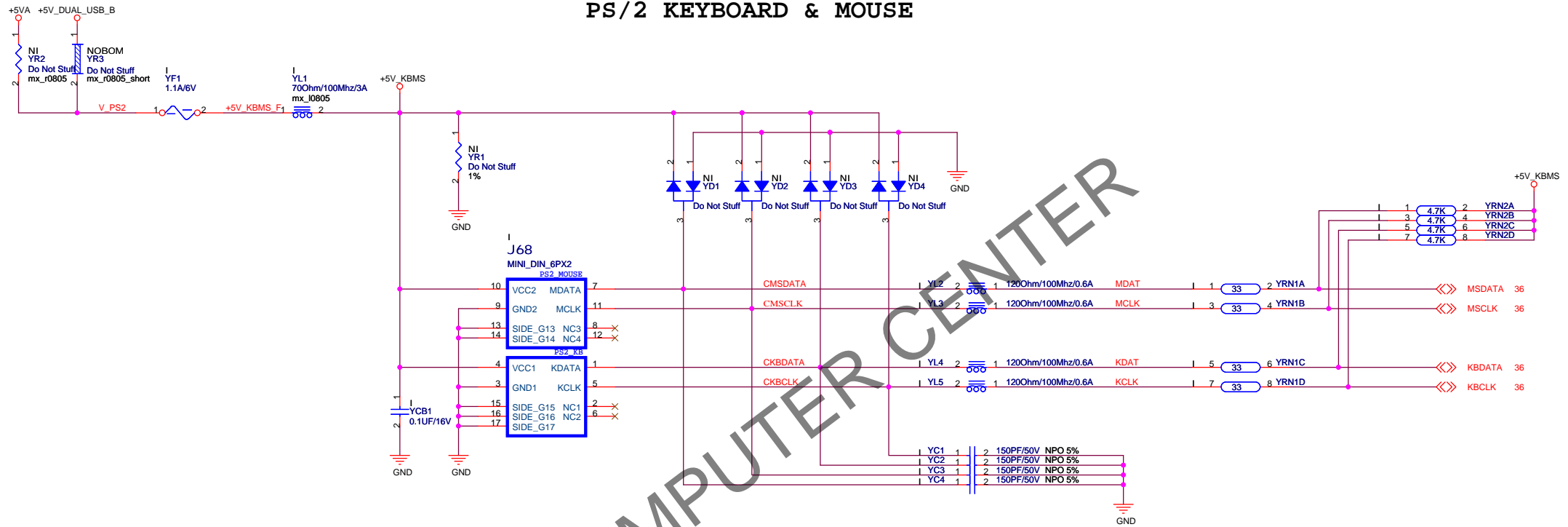
Pegatron Corp. Engineer: Tim Chen

Size	Project Name	Rev
A3	IPISB-VR	1.01

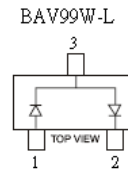
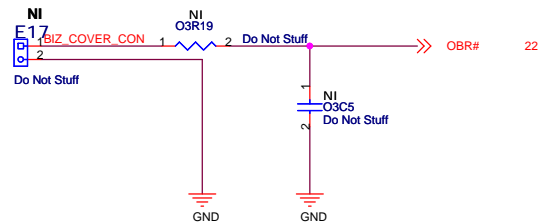
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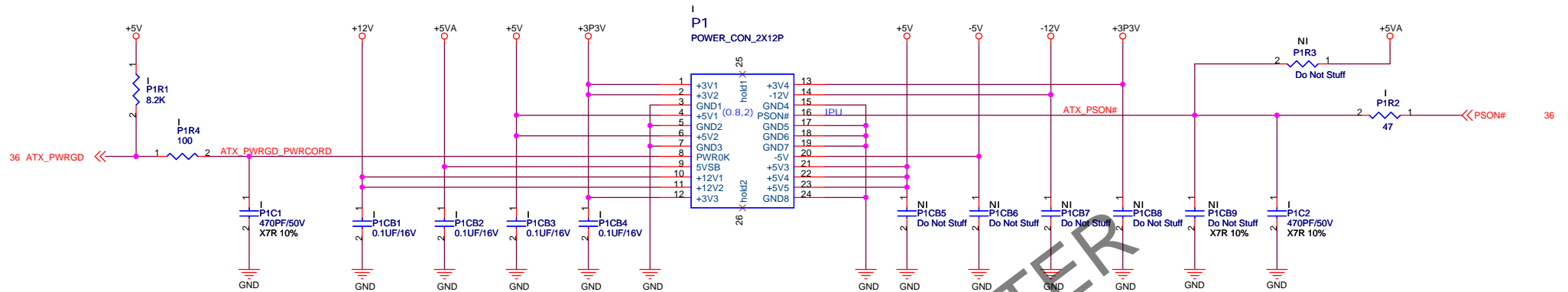
## PS/2 KEYBOARD & MOUSE



## Touch Recovery



## ATX POWER\_24P SUPPLY CONNECTOR



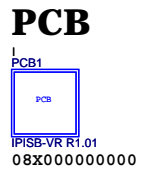
BOTTOM SIDE VIEW




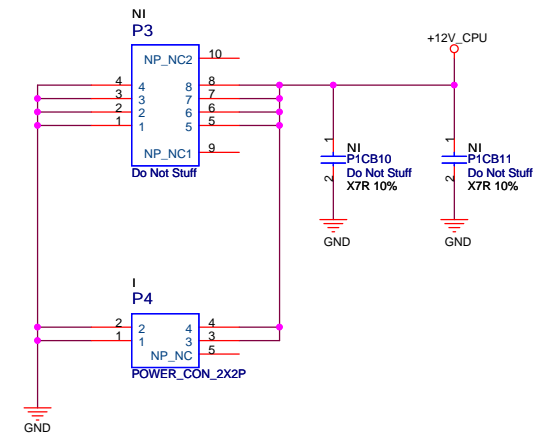
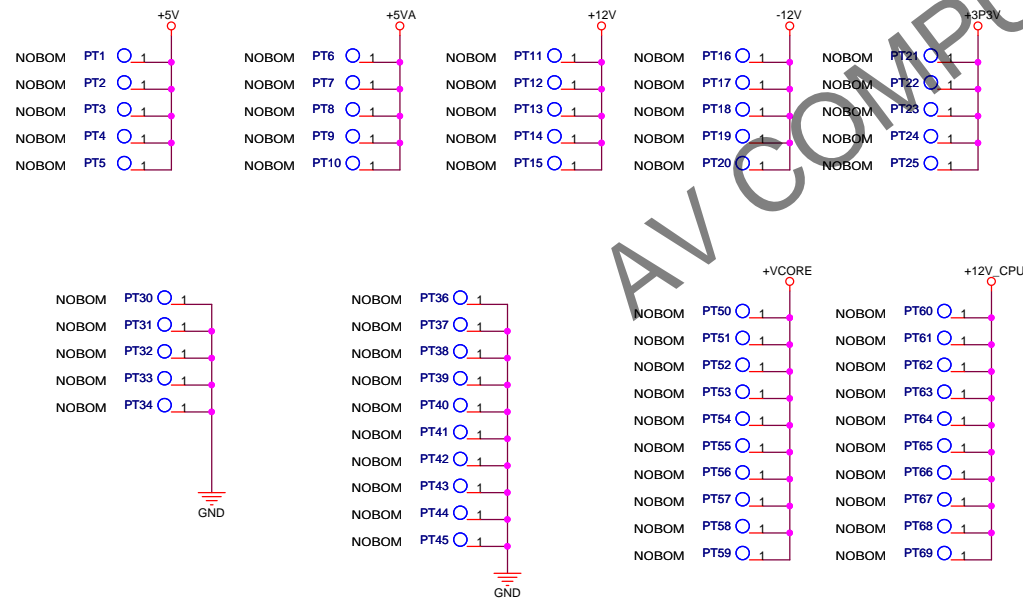
TOP SIDE VIEW



IPISB-VR R1.0  
For PVT



**PCB**  
|  
PCB1  
  
IPISB-VR R1.01  
08x00000000

**VRM POWER\_4P SUPPLY CONNECTOR**

**PEGATRON DT-MB RESTRICTED SECRET**

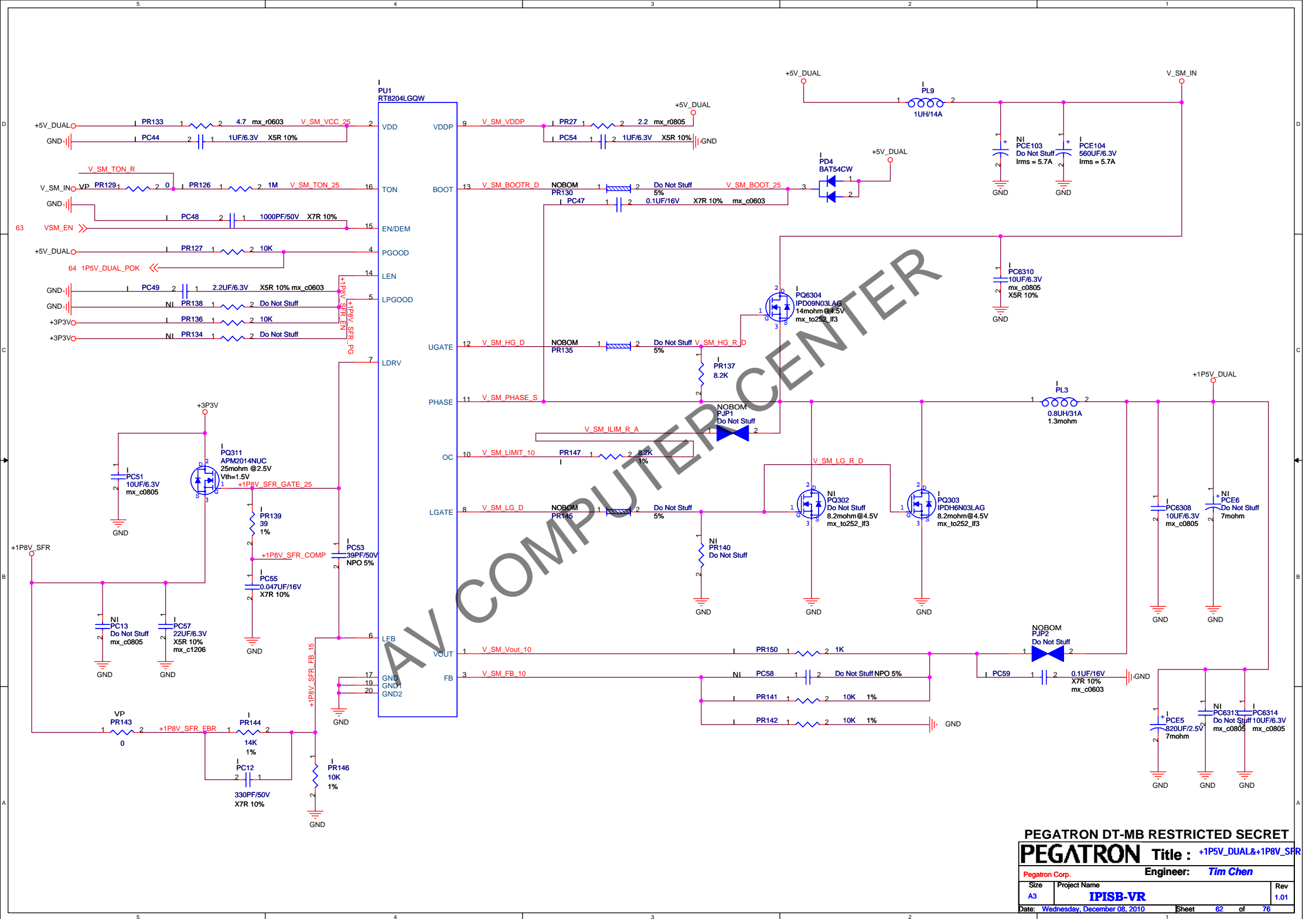
**PEGATRON** Title : ATX POWER\_24P

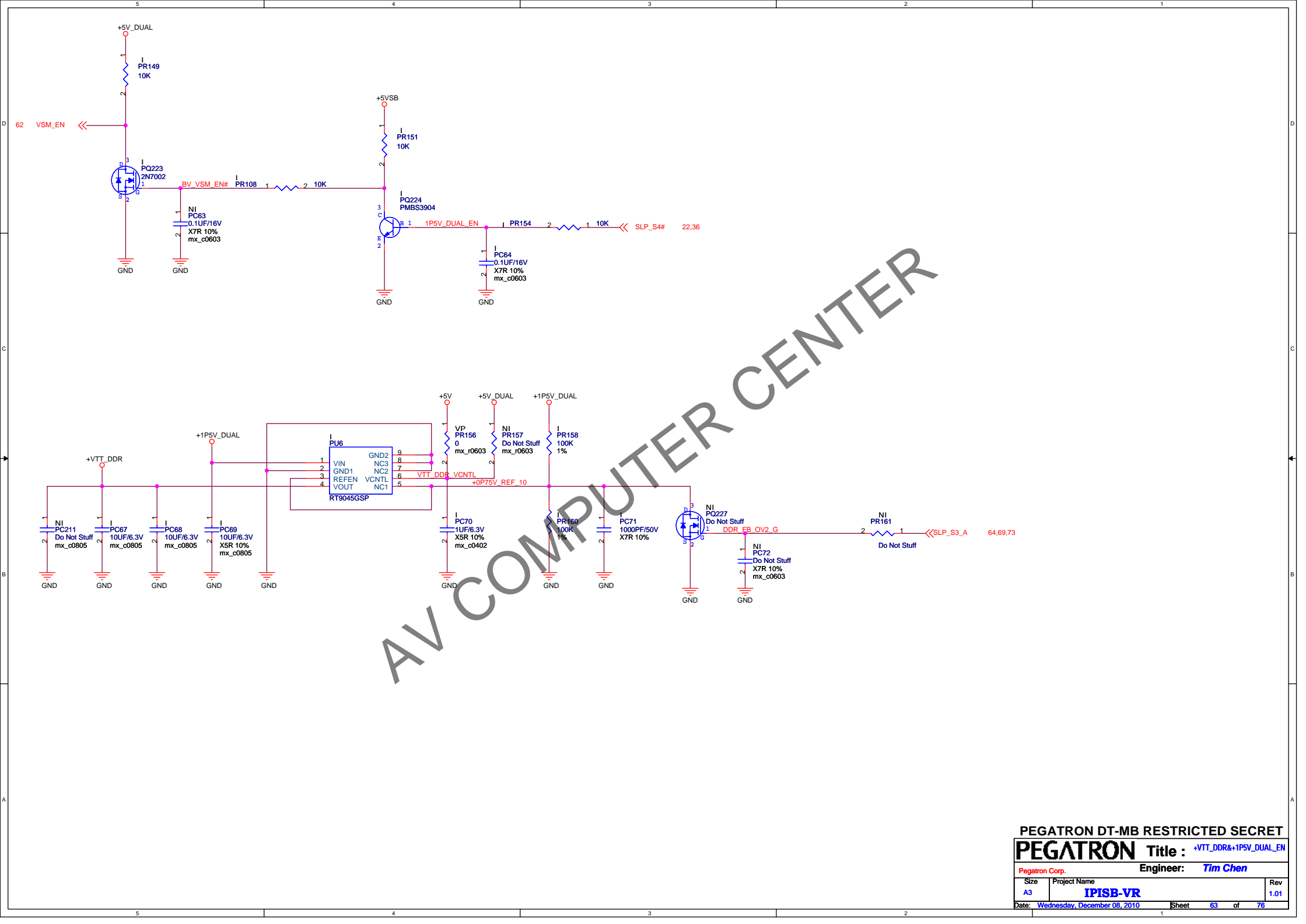
**Pegatron Corp.** **Engineer:** *Tim Chen*

Size A3	Project Name <b>IPISB-VR</b>	Rev 1.01
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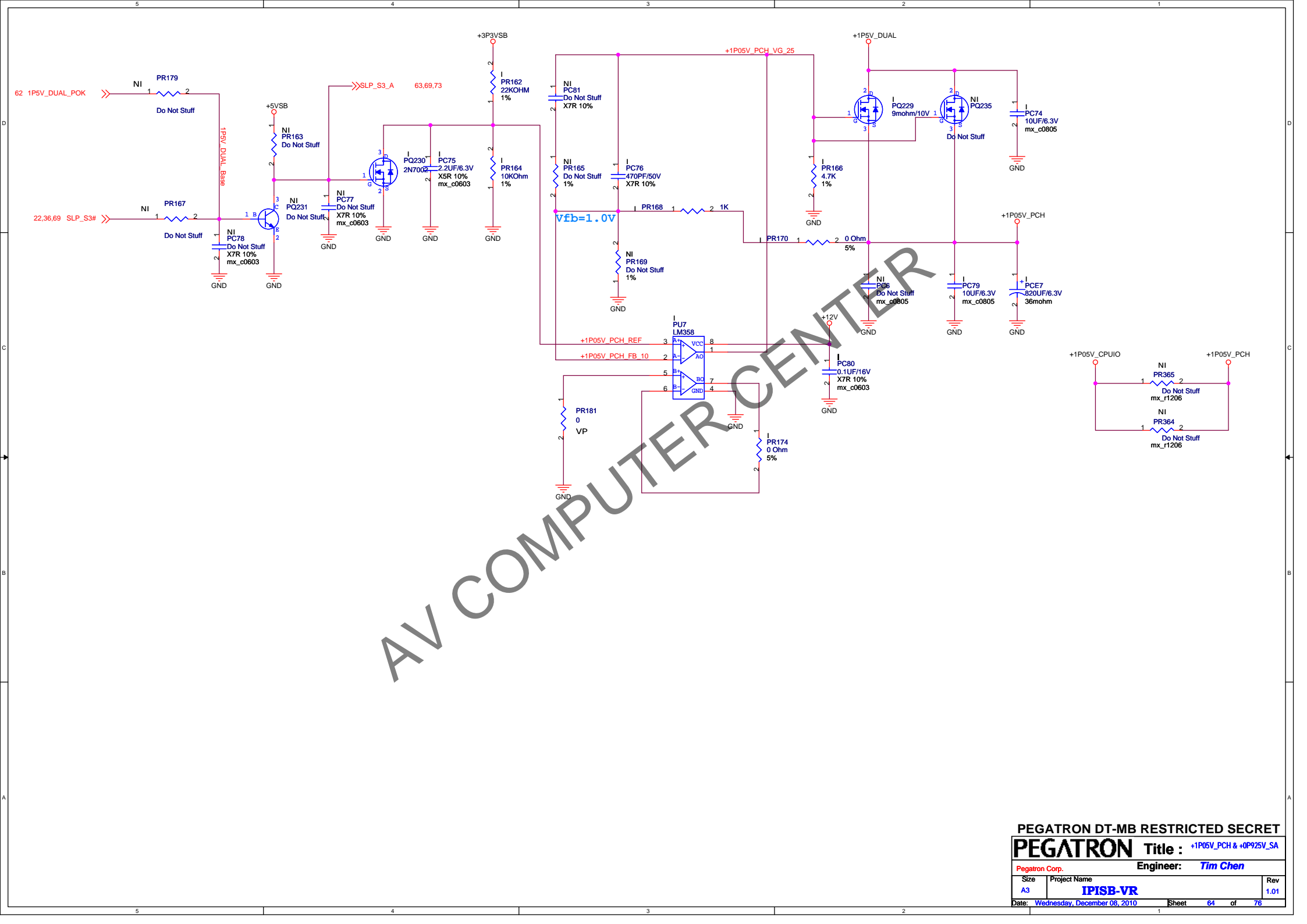
PEGATRON DT-MB RESTRICTED SECRET

**PEGATRON** Title : +VTT\_DDR&+1P5V\_DUAL\_EN

Pegatron Corp. Engineer: **Tim Chen**

Size	Project Name	Rev
A3	<b>IPISB-VR</b>	1.01

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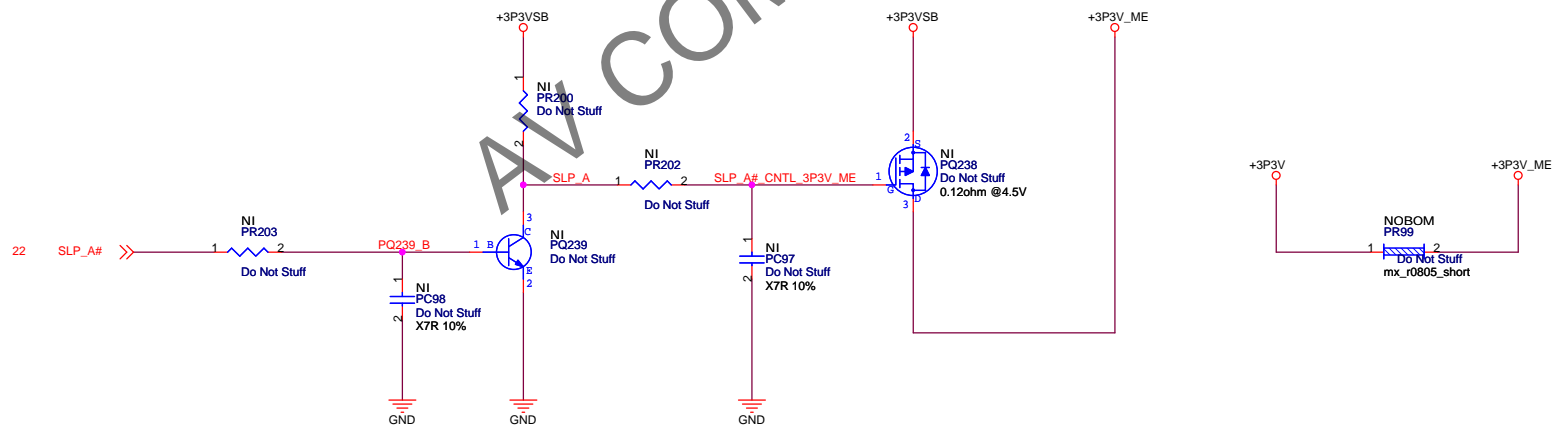
PEGATRON DT-MB RESTRICTED SECRET

**PEGATRON** Title : XXXX

Pegatron Corp. Engineer: Tim Chen

Size A3	Project Name <b>IPISB-VR</b>	Rev 1.01
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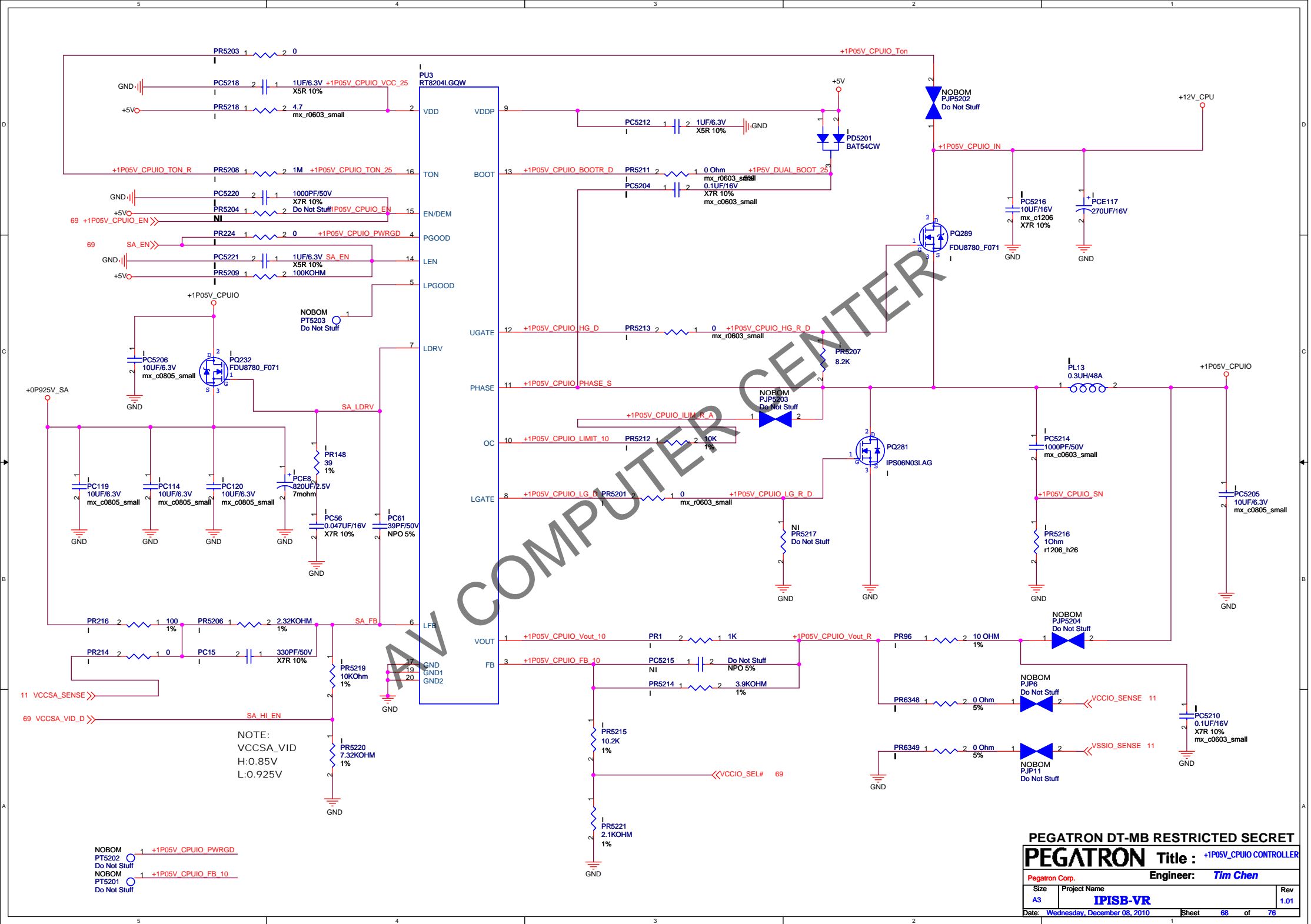
PEGATRON DT-MB RESTRICTED SECRET

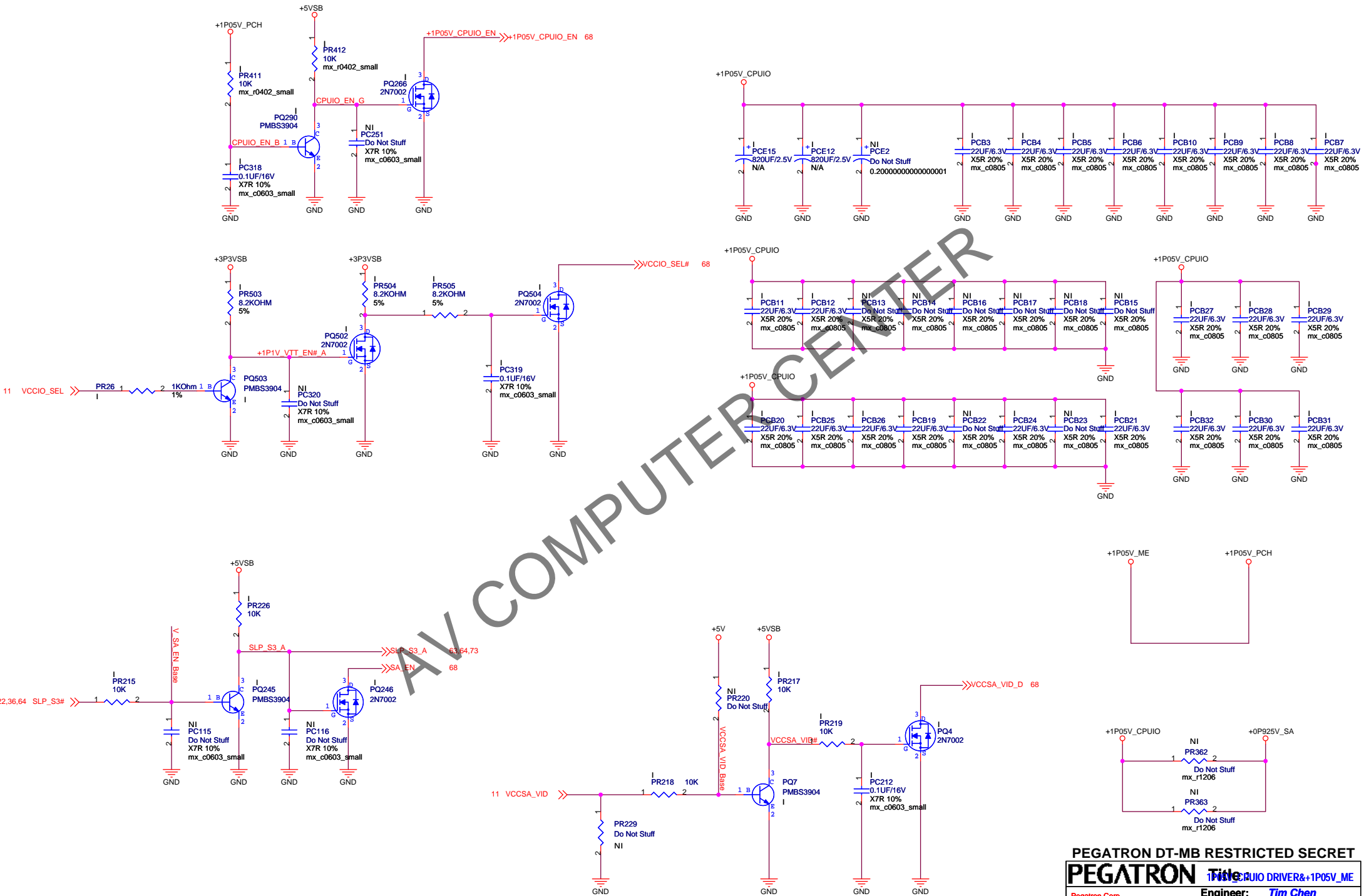
**PEGATRON** Title : +3P3V\_ME

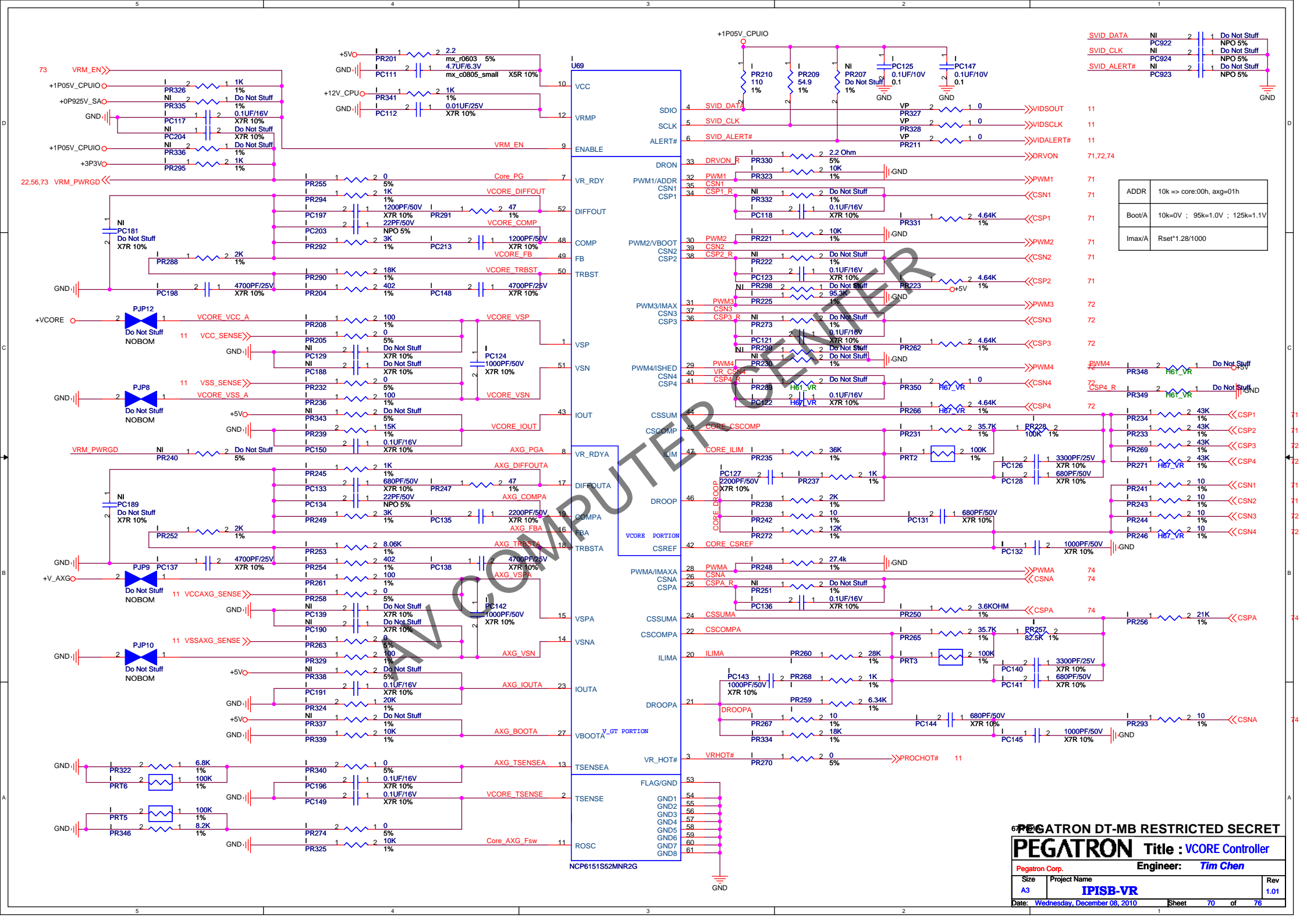
Pegatron Corp. Engineer: Tim Chen

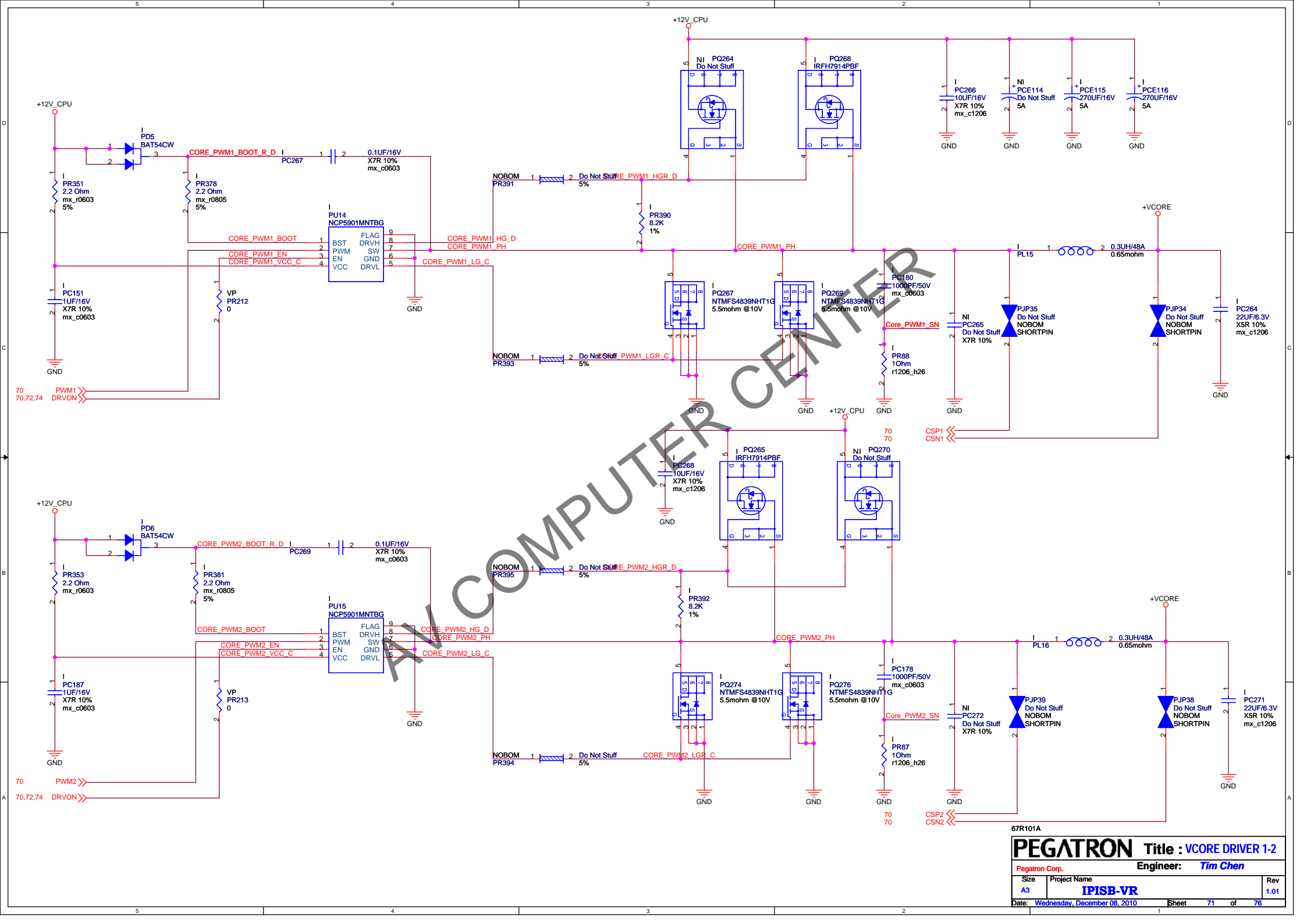
Size A3	Project Name <b>IPISB-VR</b>	Rev 1.01
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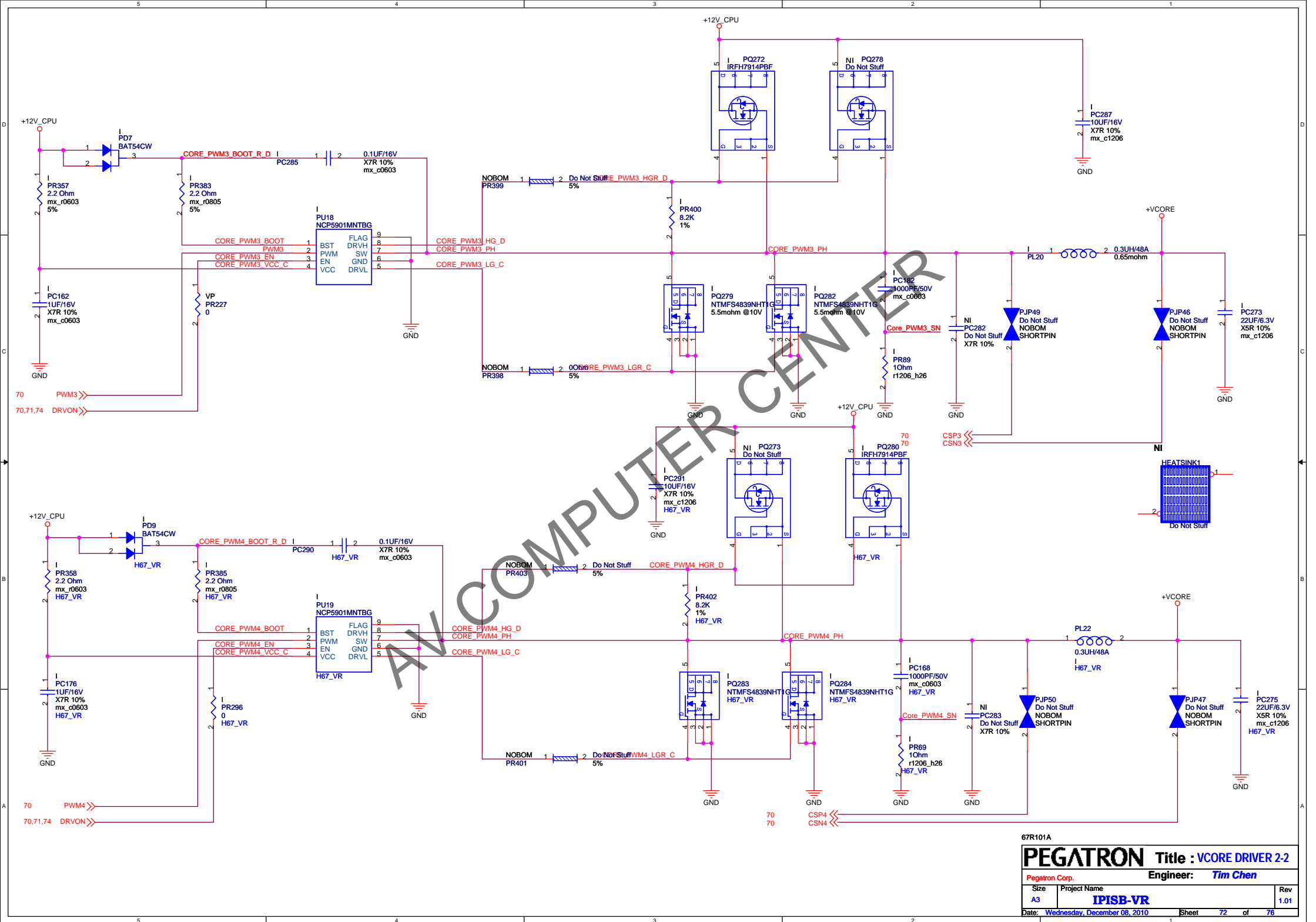
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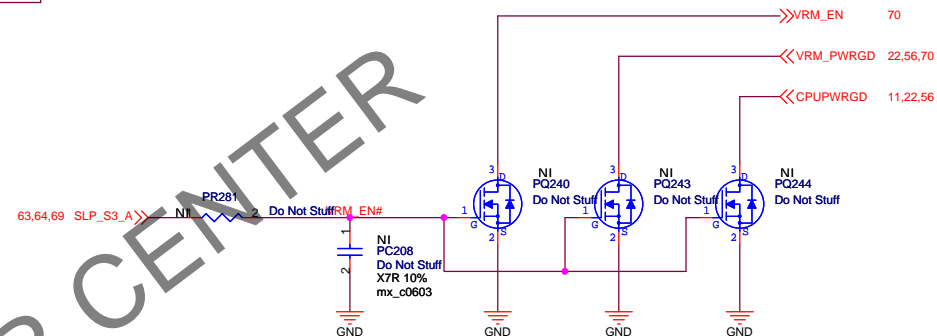
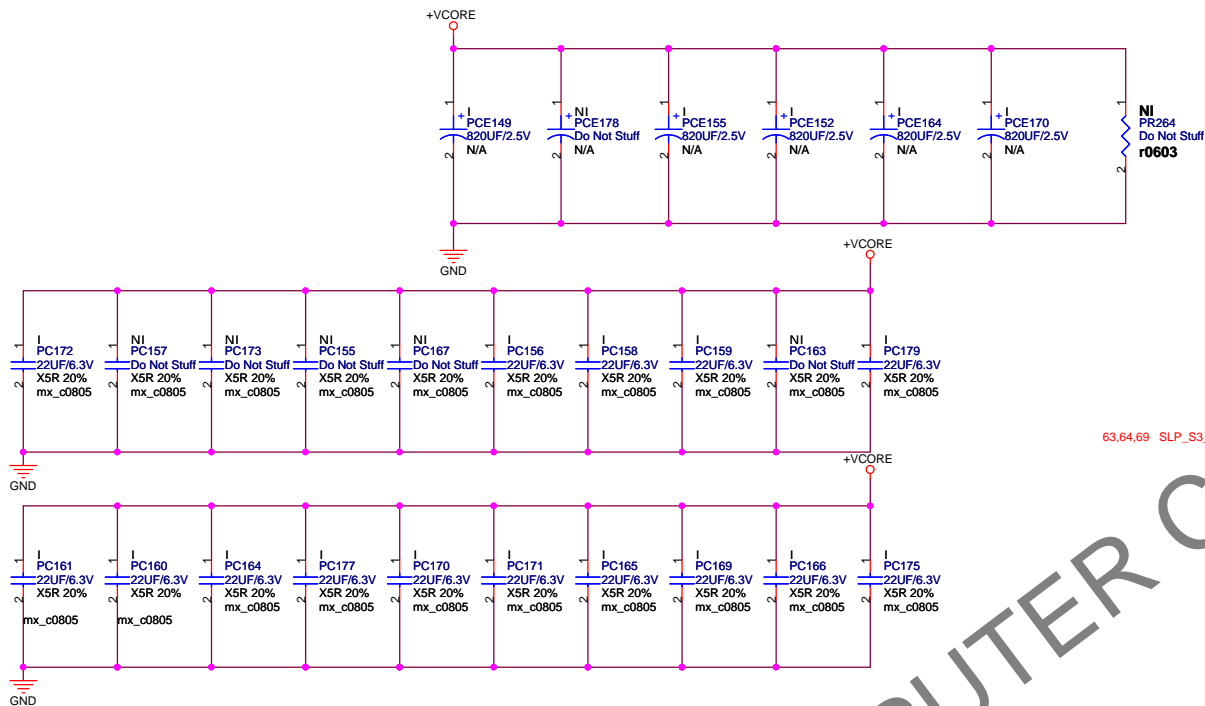












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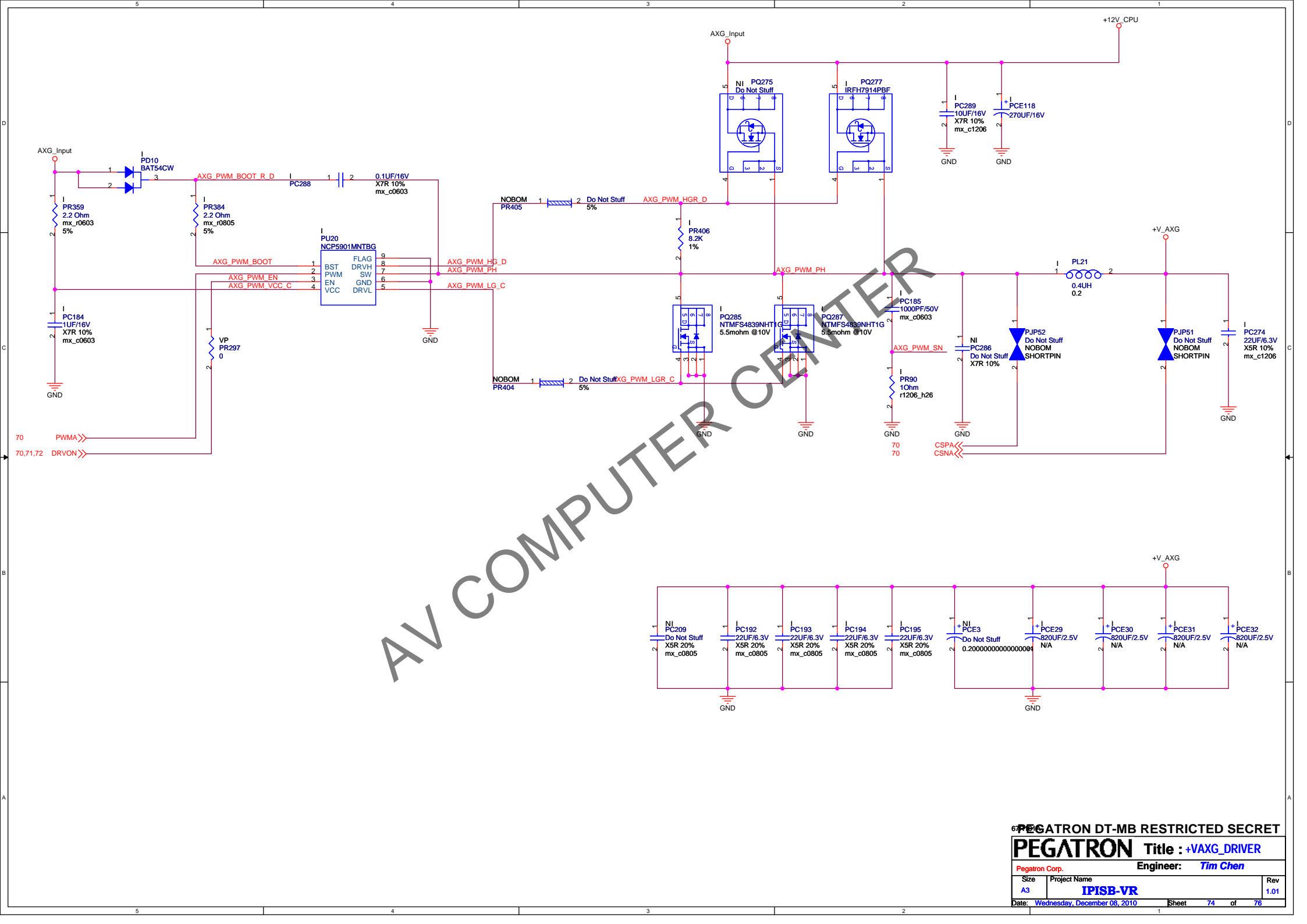
PEGATRON DT-MB RESTRICTED SECRET

**PEGATRON** Title : V CORE DRIVER 2-2

Pegatron Corp. Engineer: Tim Chen

Size	Project Name	Rev
A3	IPISB-VR	1.01

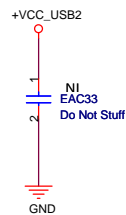
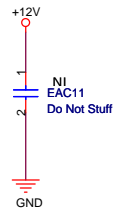
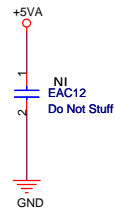
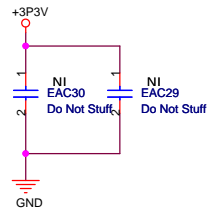
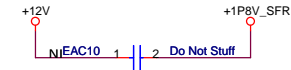
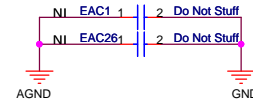
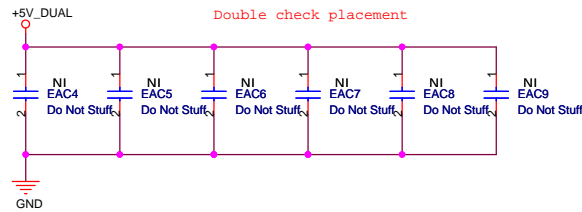
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67 PEGATRON DT-MB RESTRICTED SECRET

<b>PEGATRON</b>		Title : <b>OV Controller</b>	
PEGATRON CORP.		Engineer: <b>Tim Chen</b>	
Size A3	Project Name <b>IPISB-VR</b>		Rev 1.01
Date: <b>Wednesday, December 08, 2010</b>		Sheet <b>75</b> of <b>76</b>	



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